



1/52

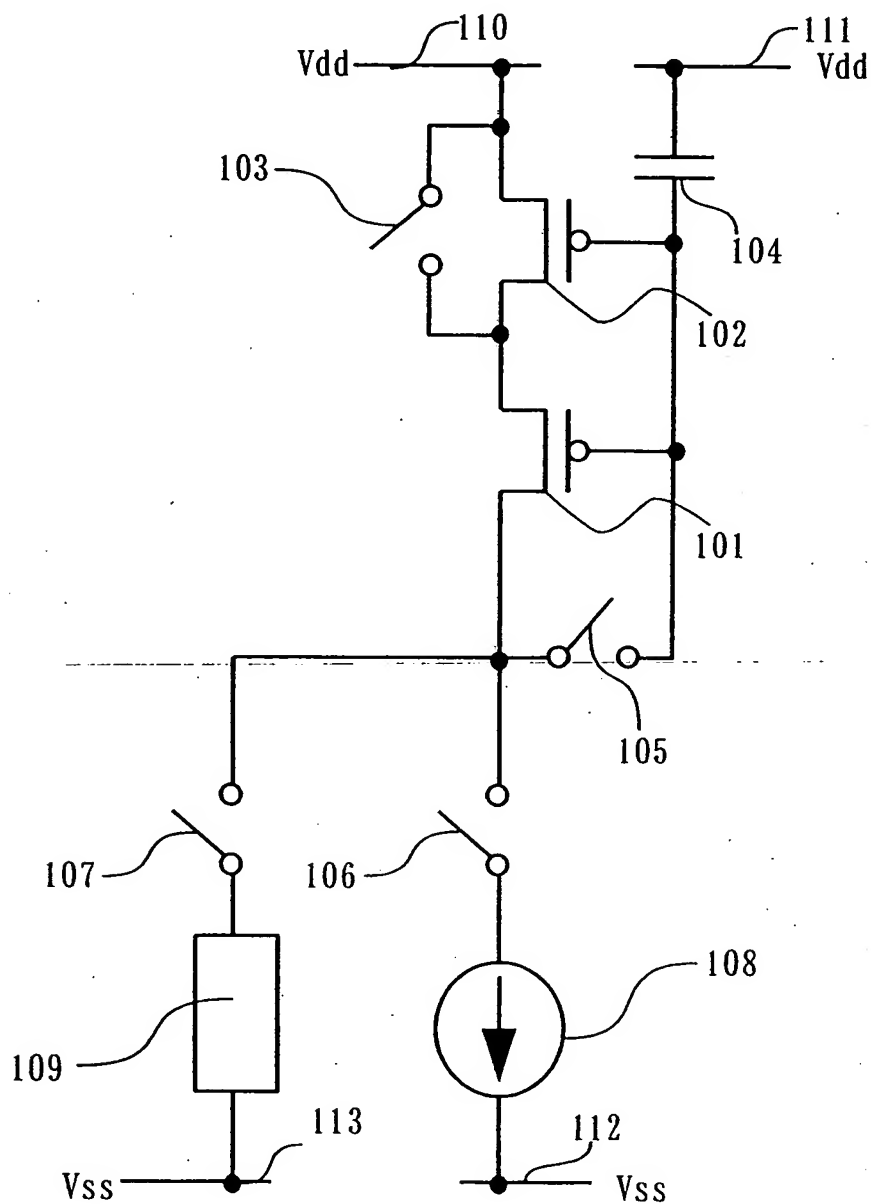


FIG. 1

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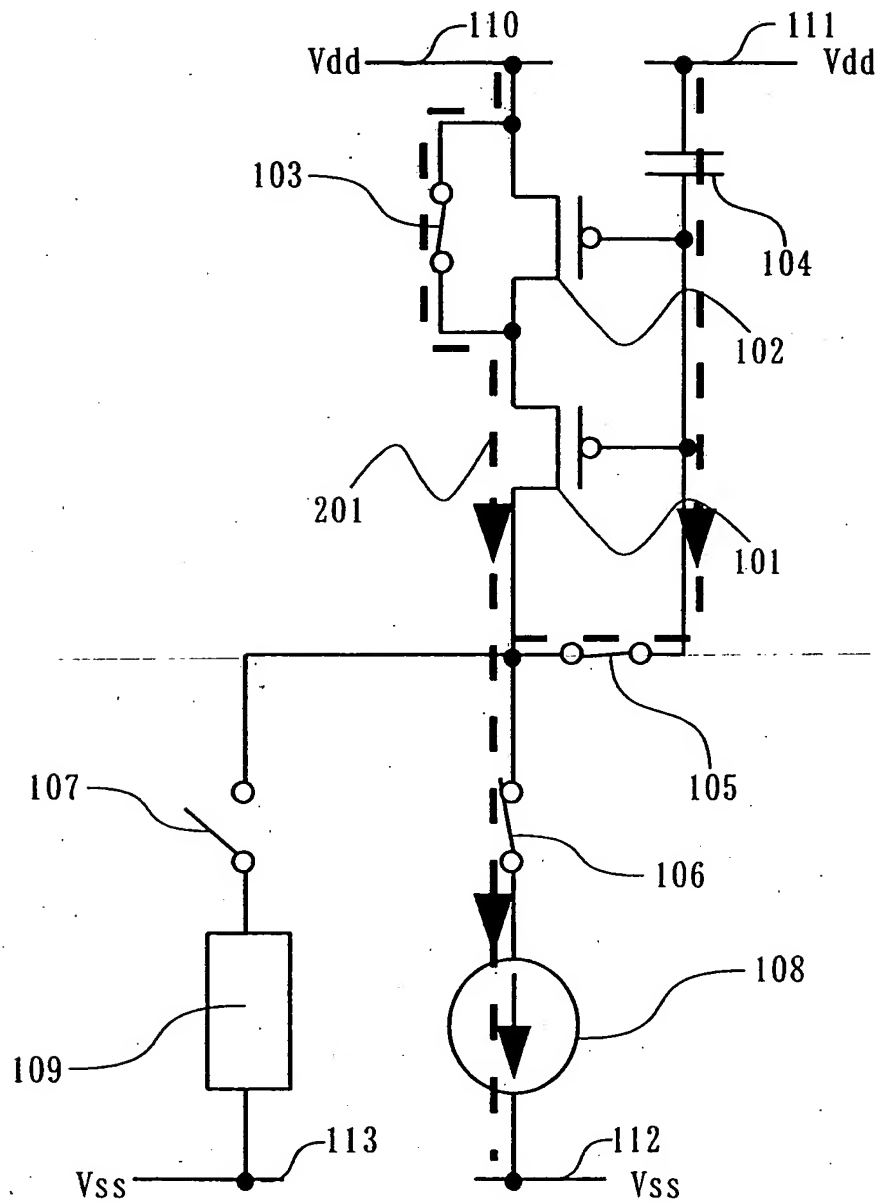


FIG. 2

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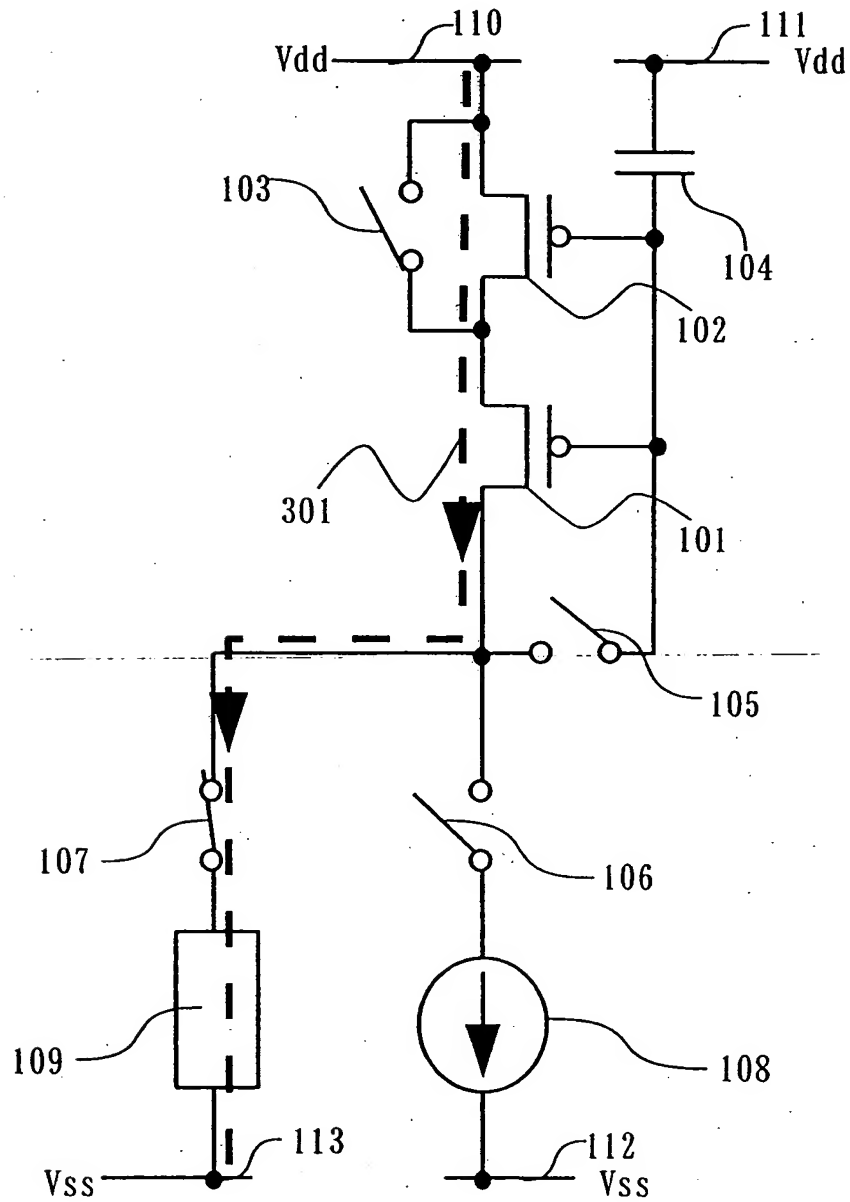


FIG. 3

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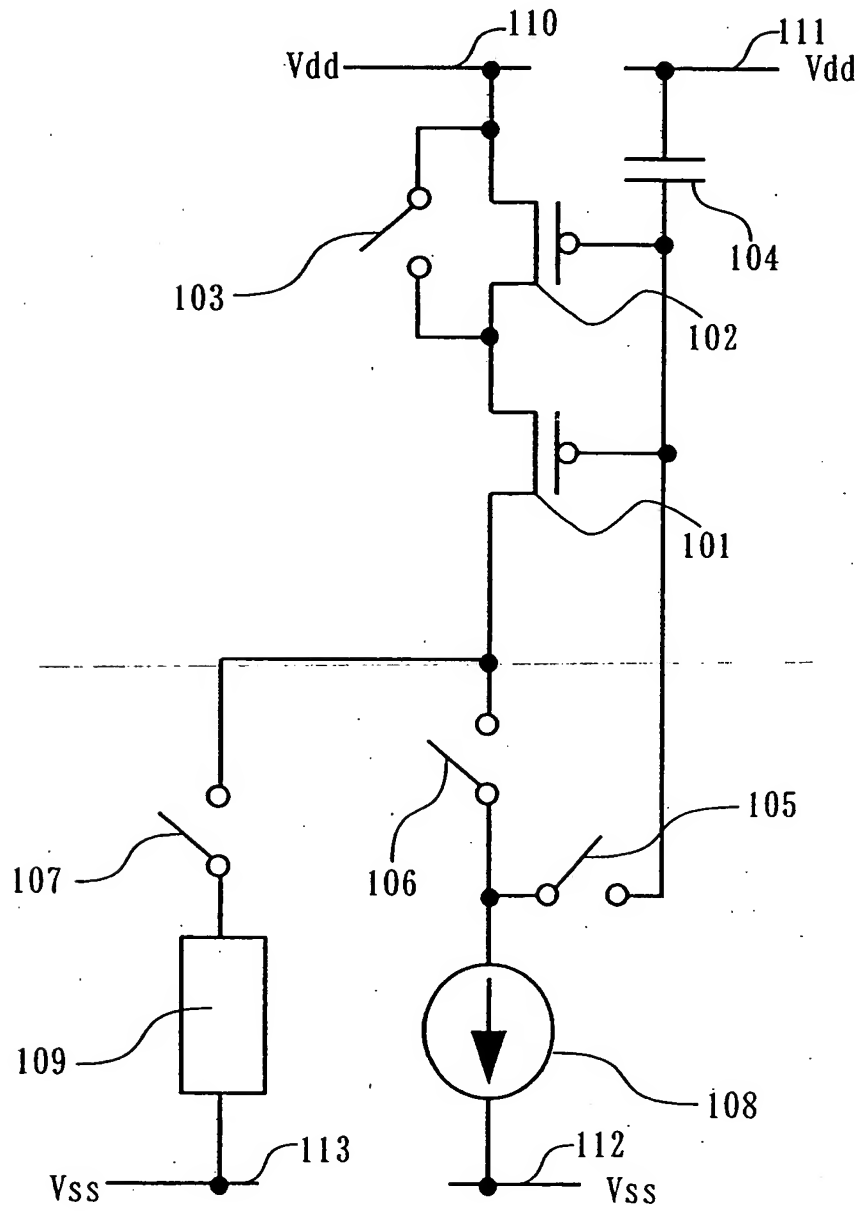


FIG. 4

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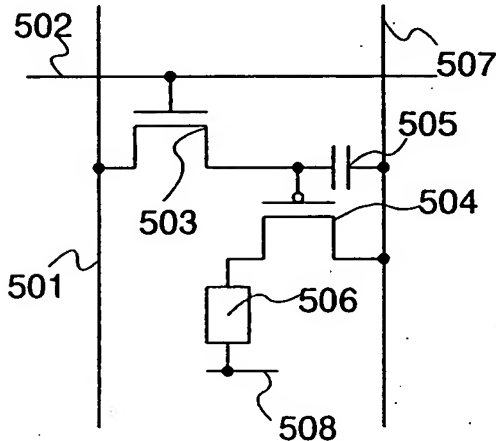


FIG. 5

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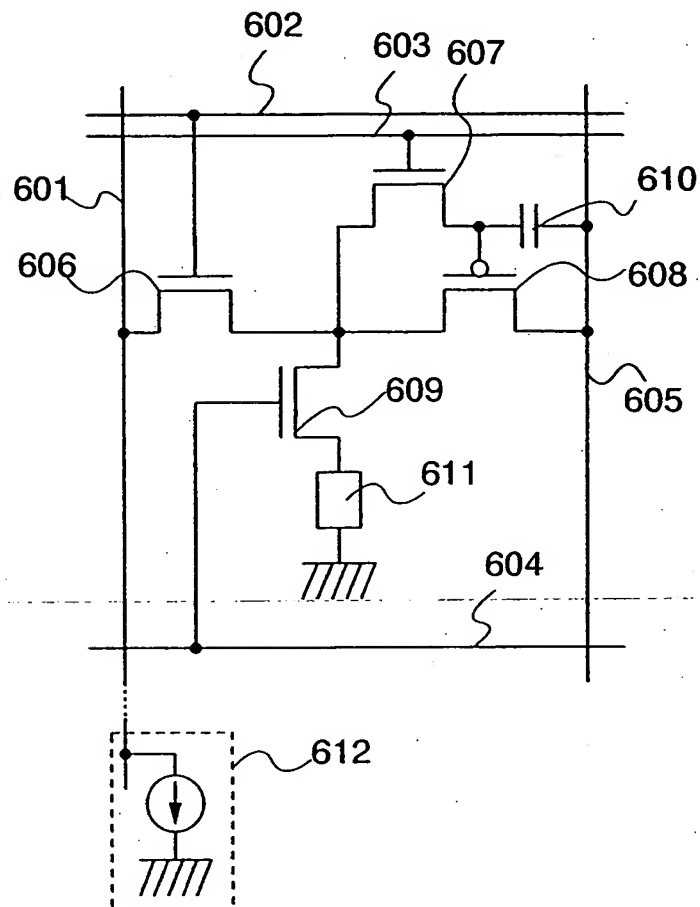
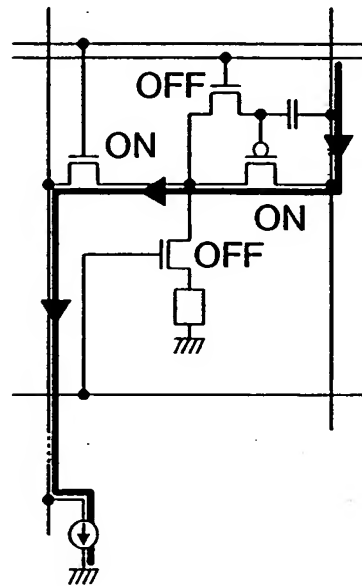
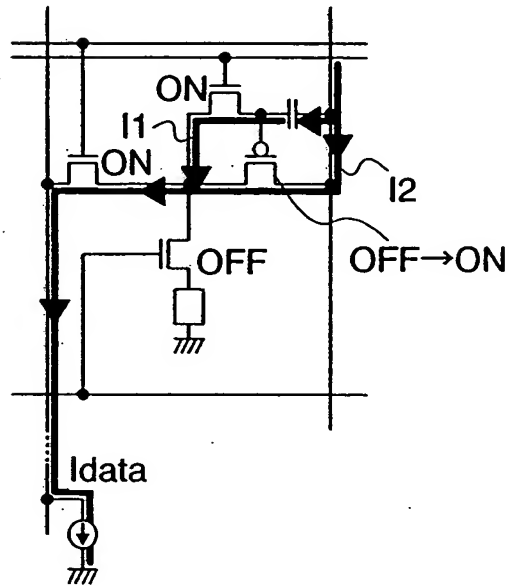


FIG. 6

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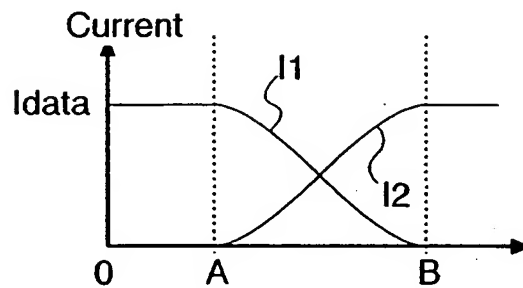
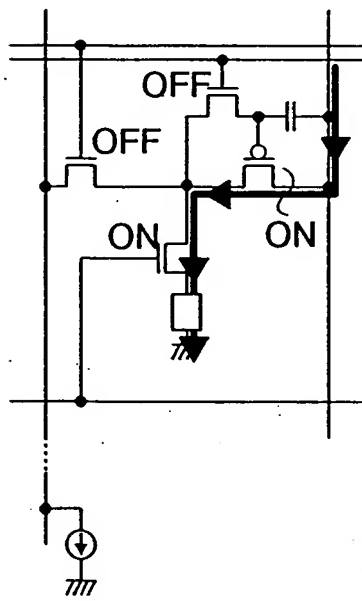
(A) In inputting a signal

(B) In completing to input a signal



(C) In emitting light

(D)



(E)

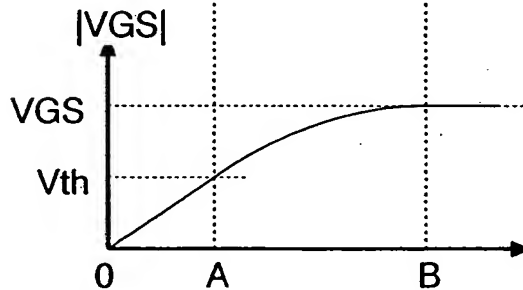


FIG. 7

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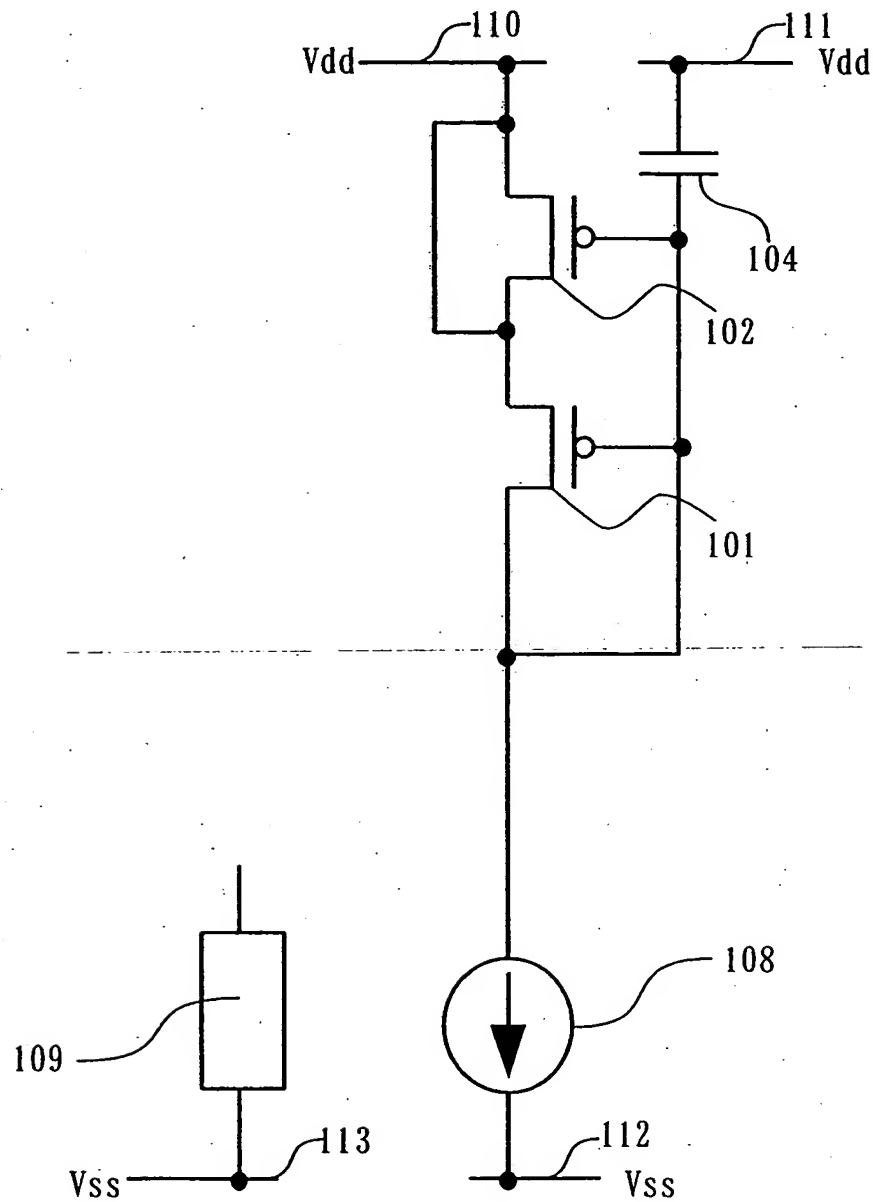


FIG. 8

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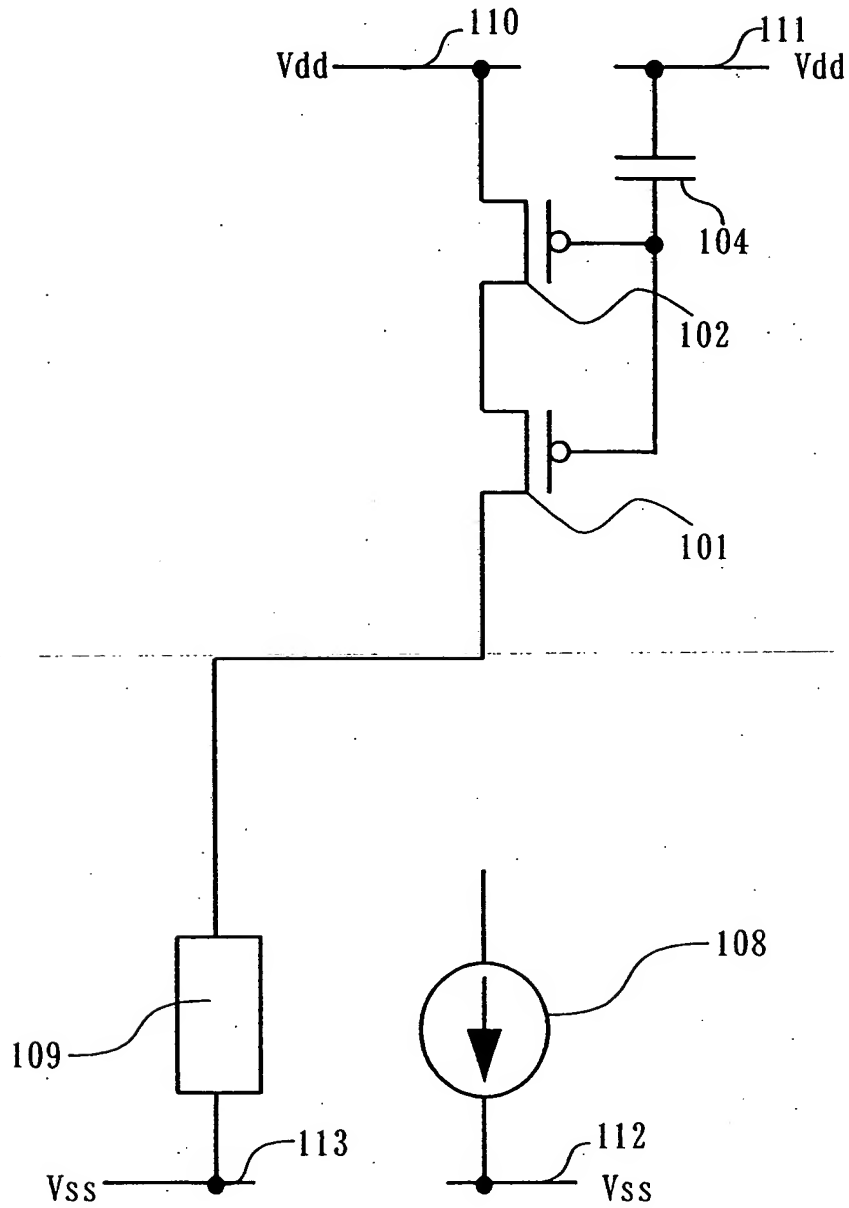


FIG. 9

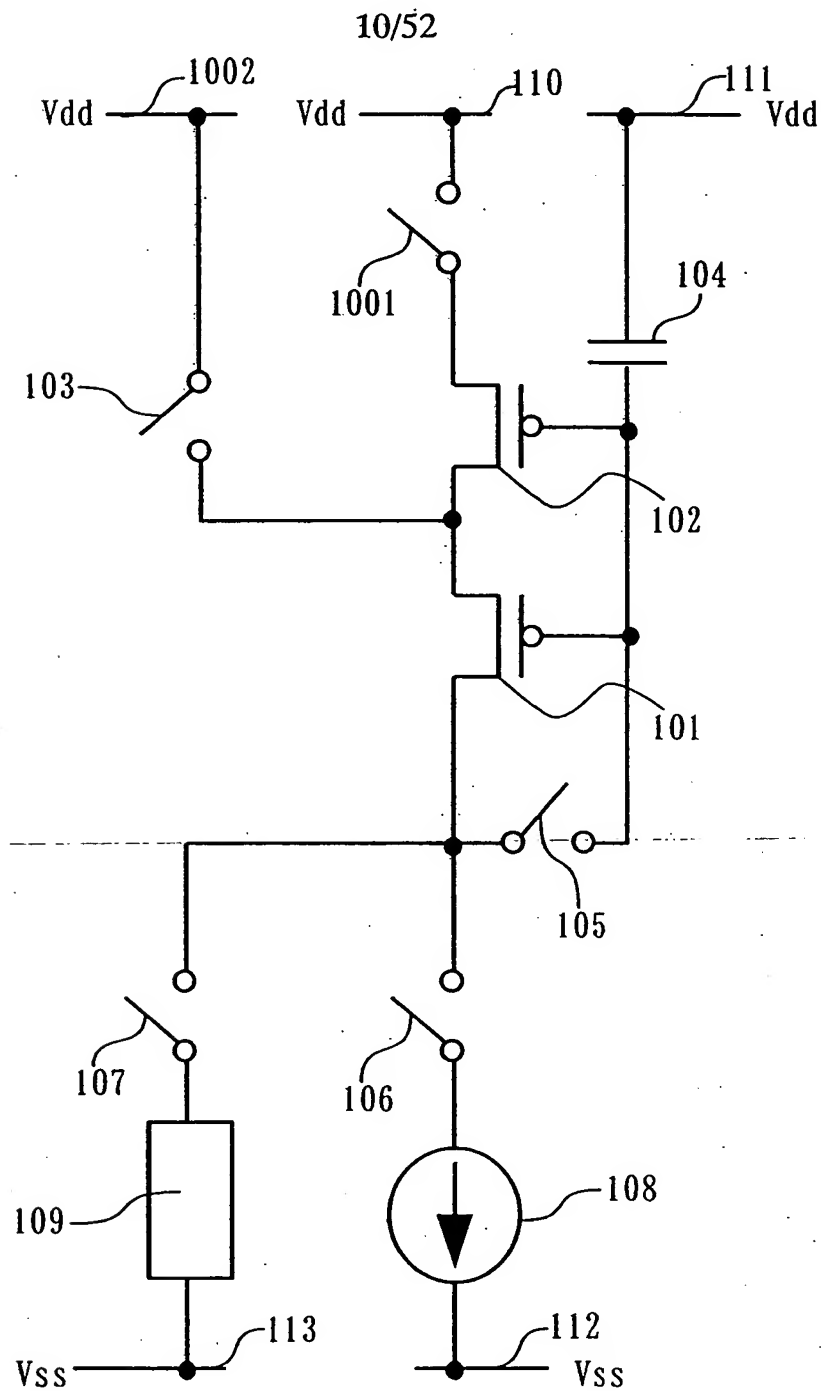


FIG.10



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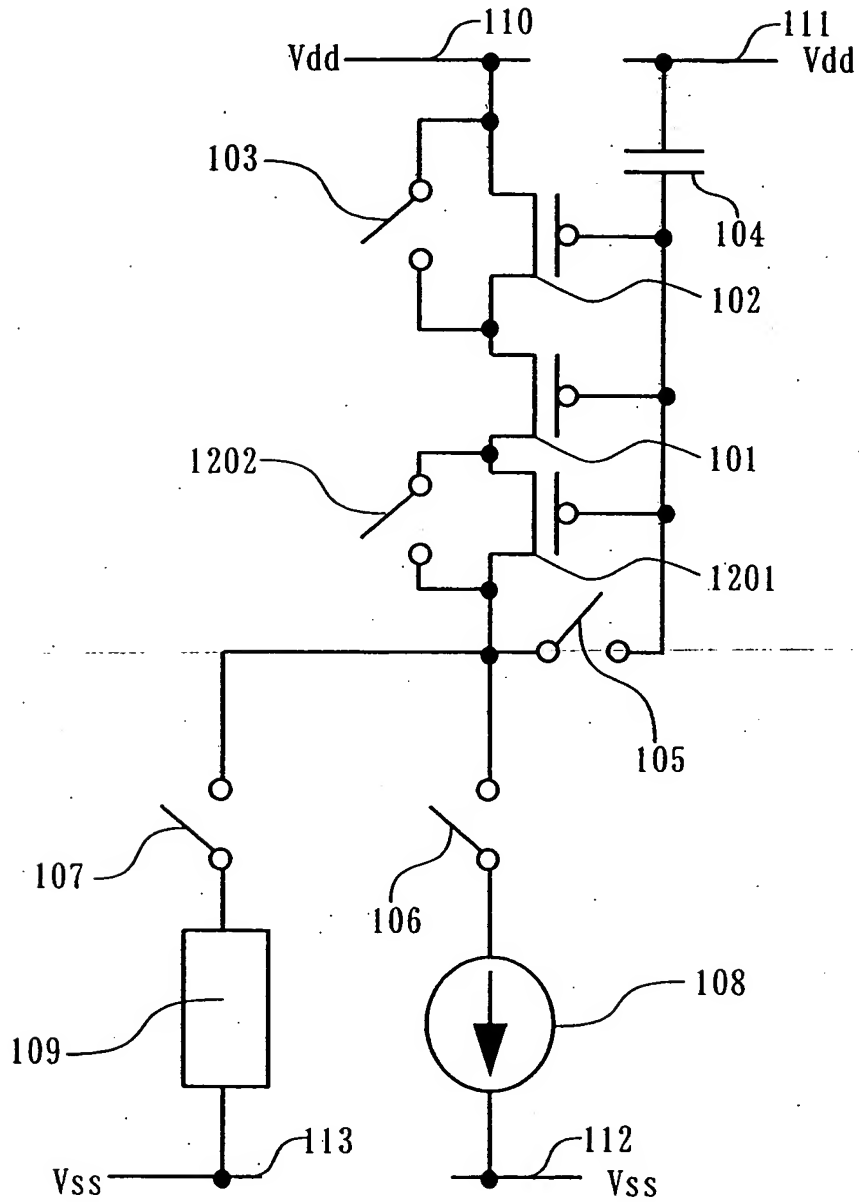


FIG. 12

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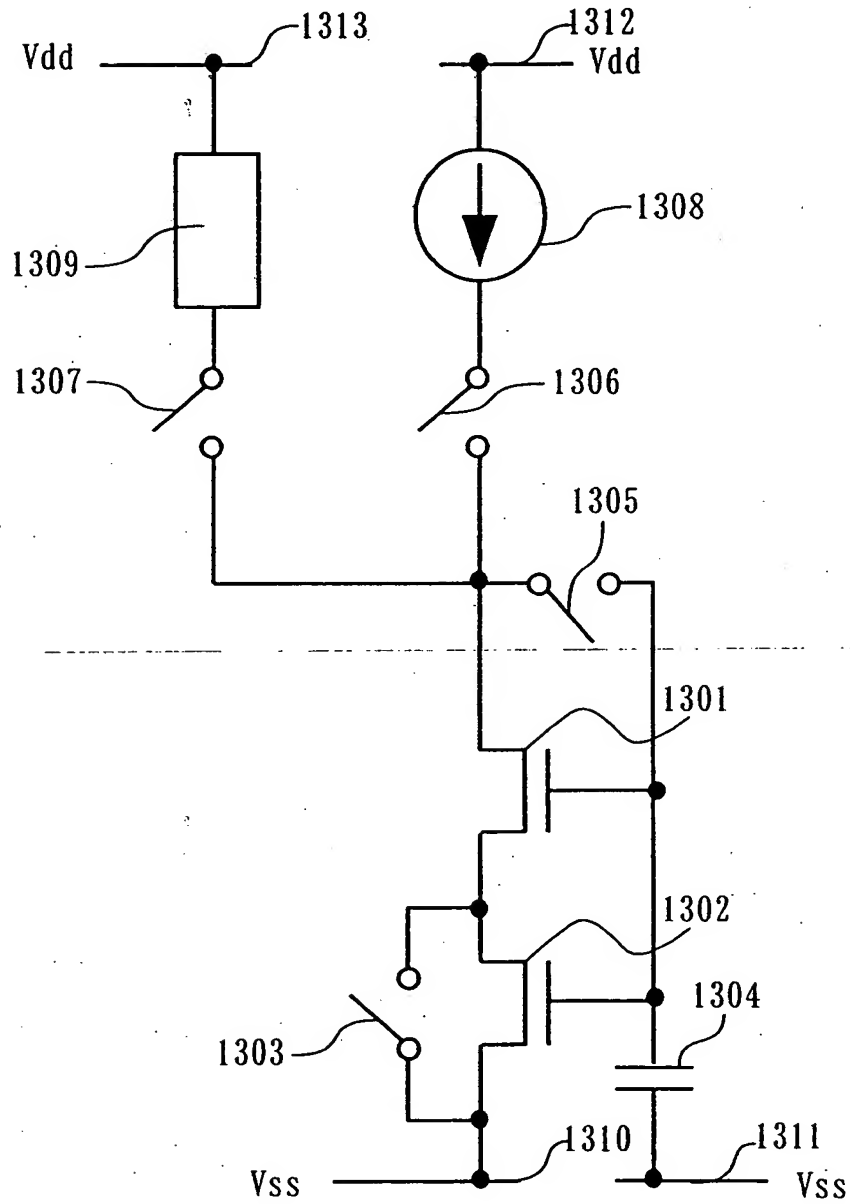


FIG. 13

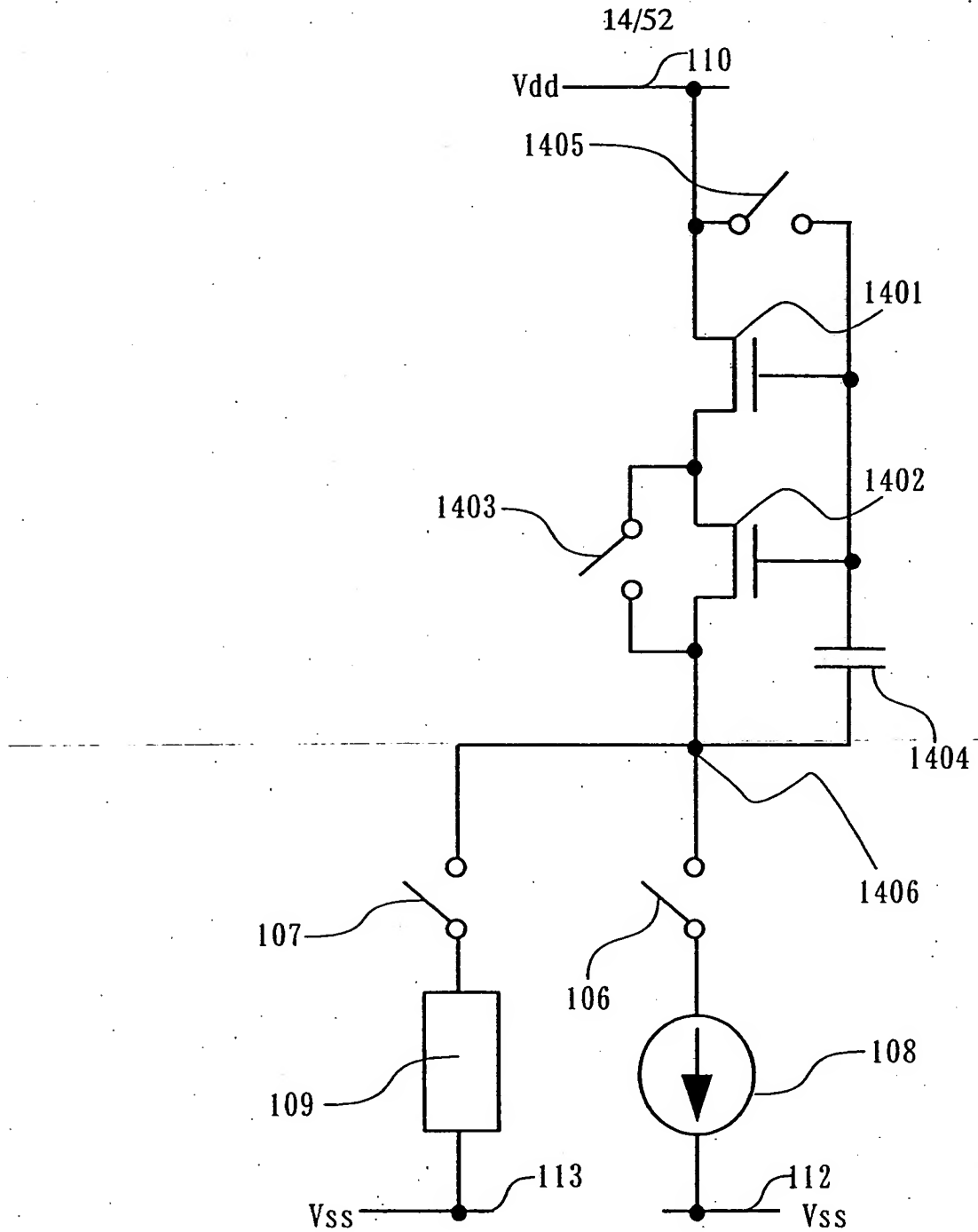


FIG. 14

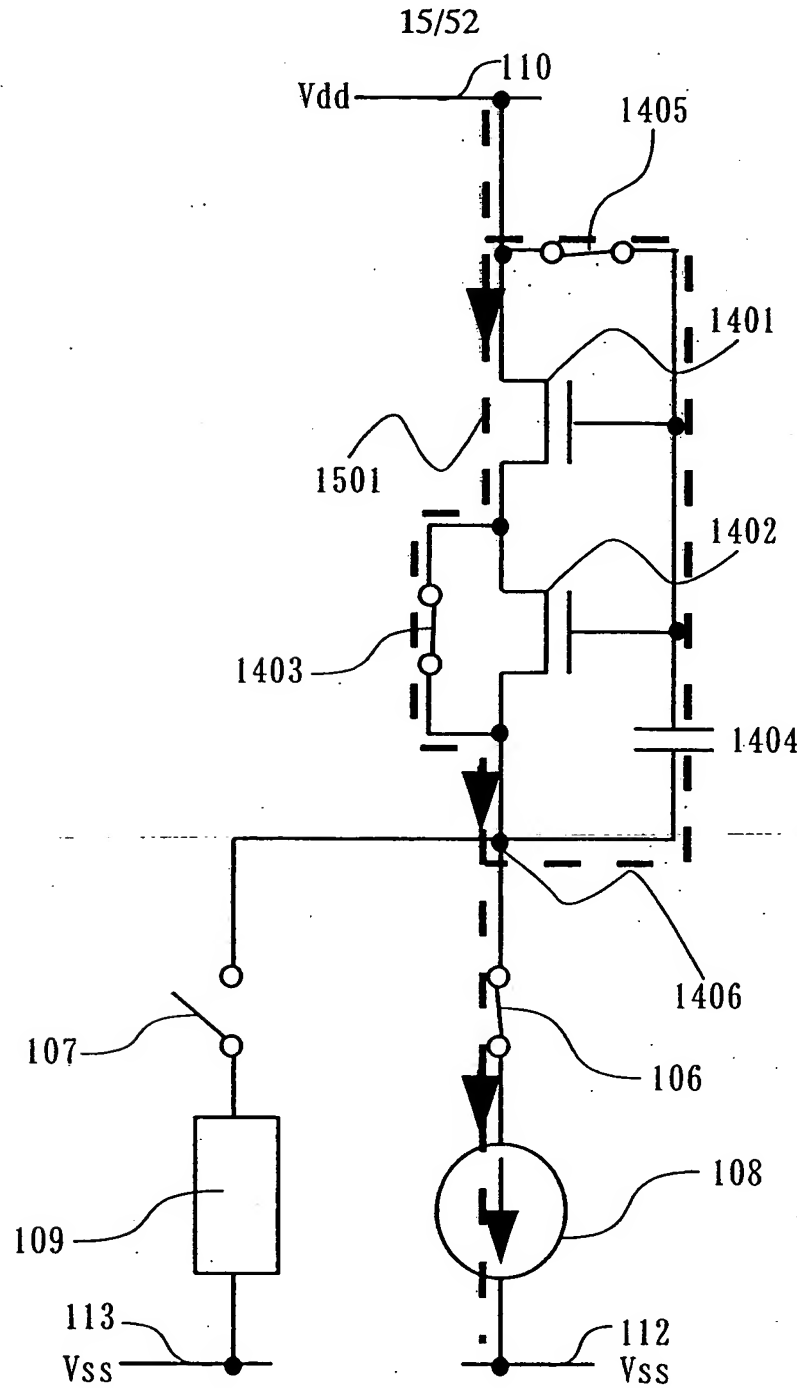


FIG. 15

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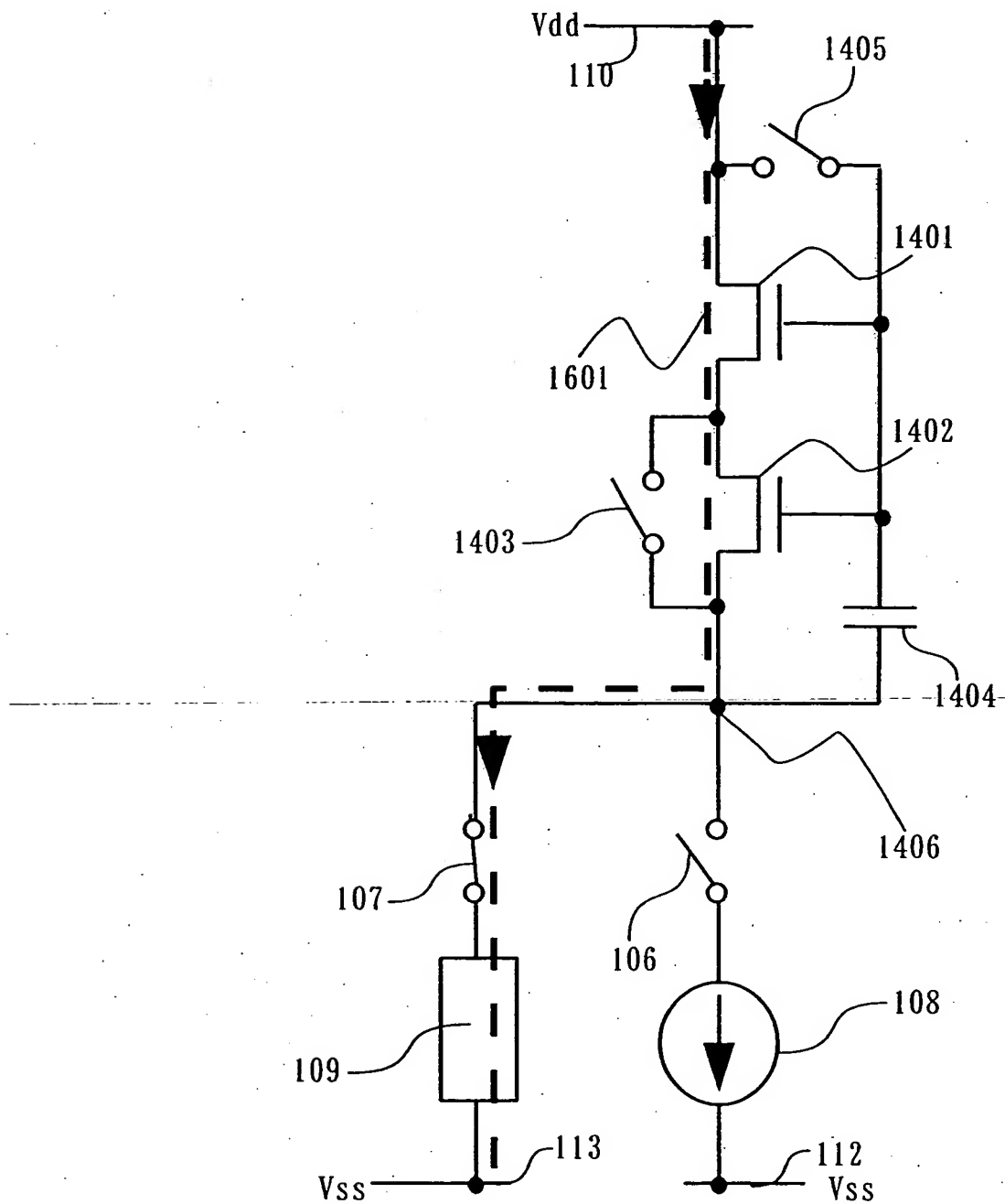


FIG. 16

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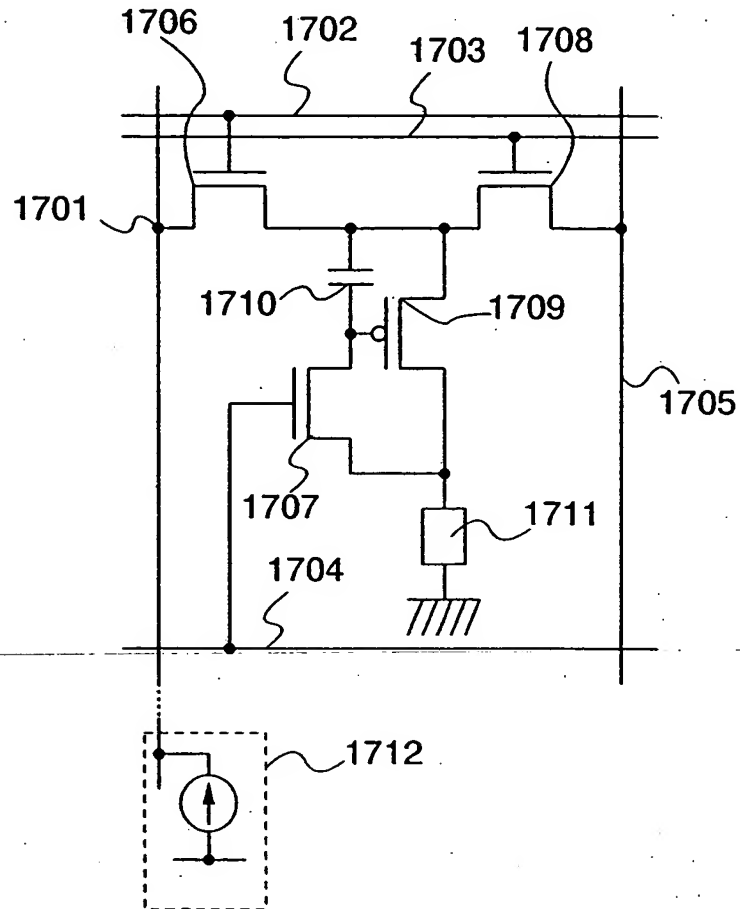
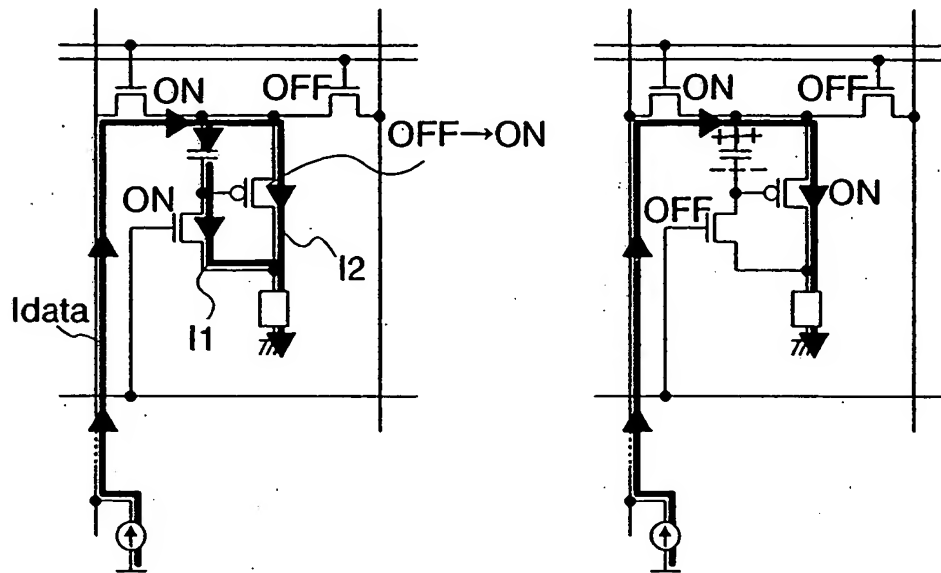
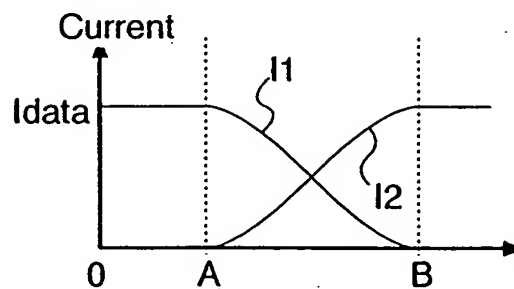
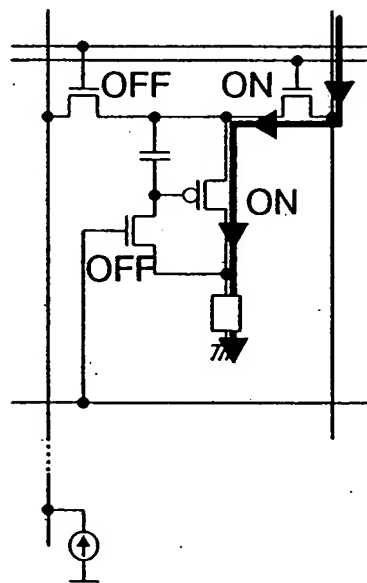


FIG. 17

(B) In completing to input a signal



(D)



(E)

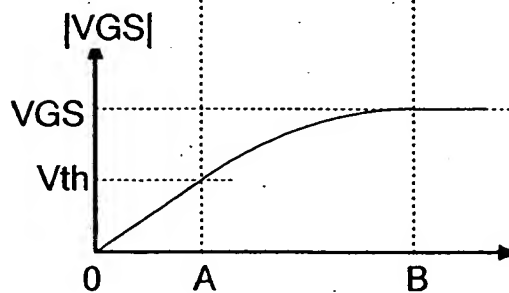


FIG. 18

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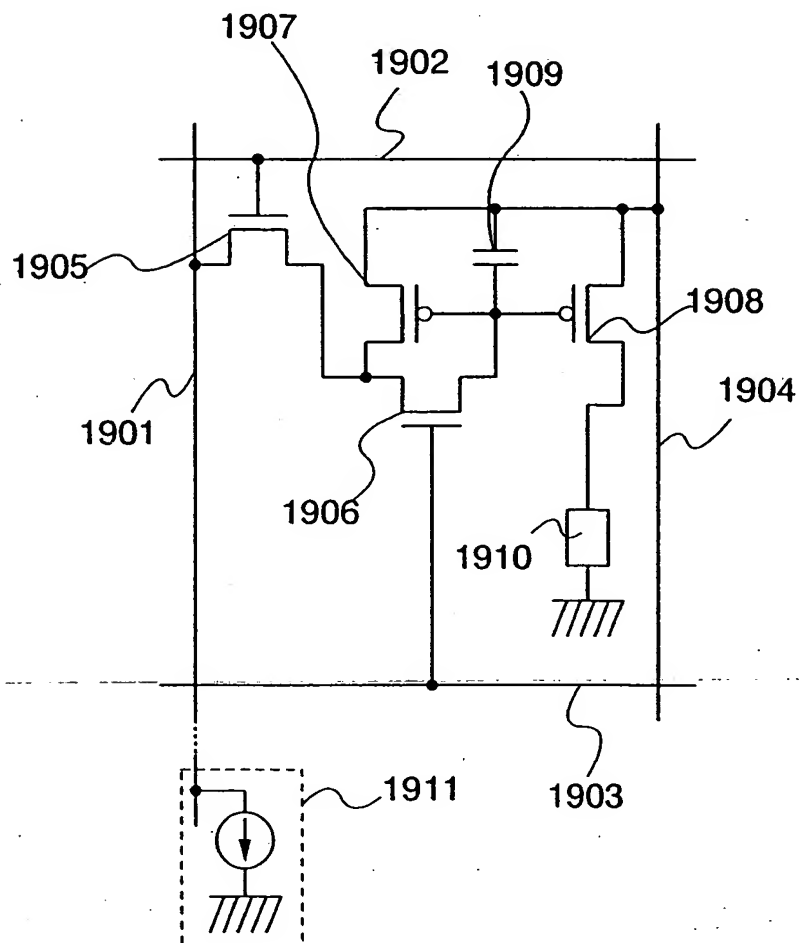
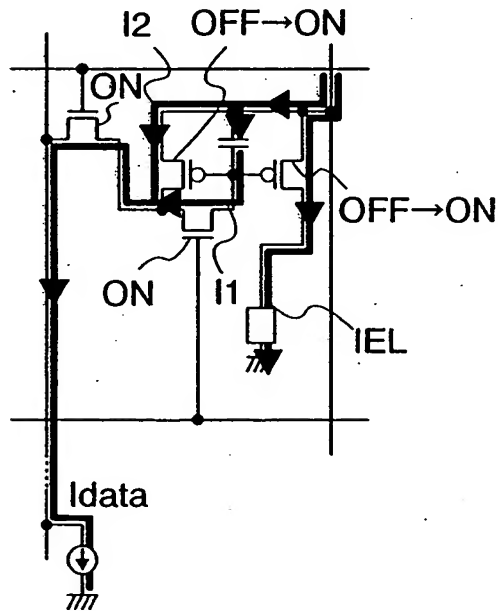


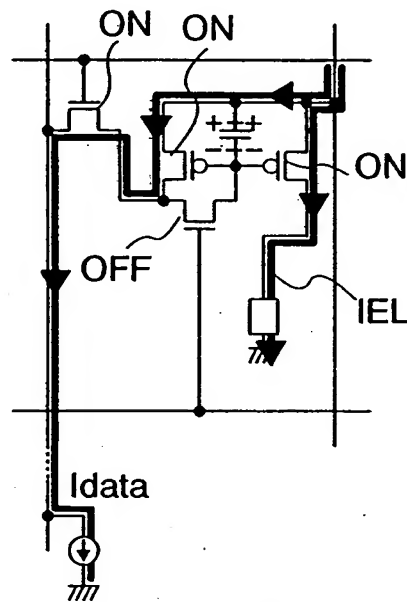
FIG. 19

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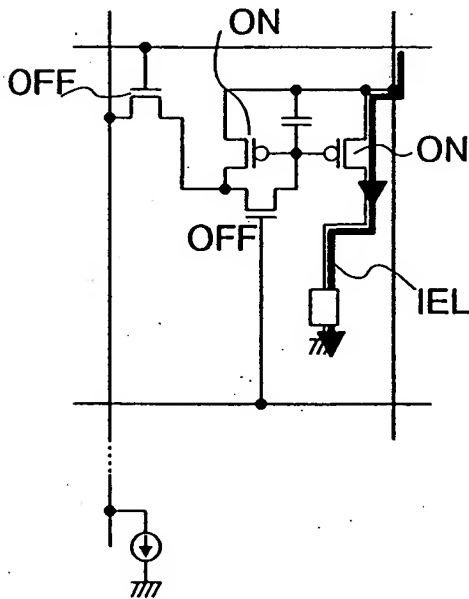
(A) In inputting a signal



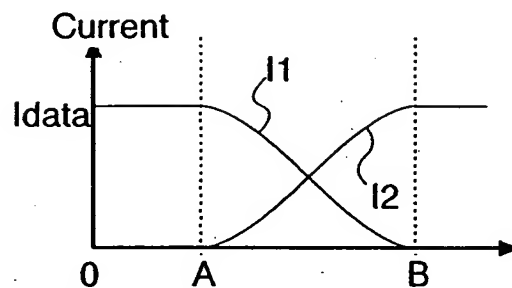
(B) In completing to input a signal



(C) In emitting light



(D)



(E)

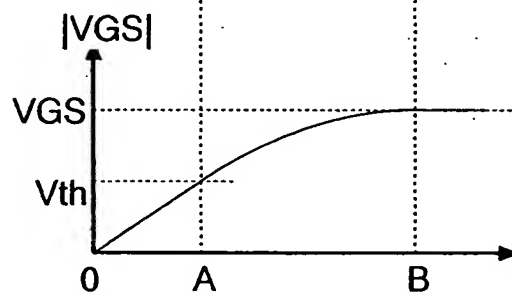


FIG. 20

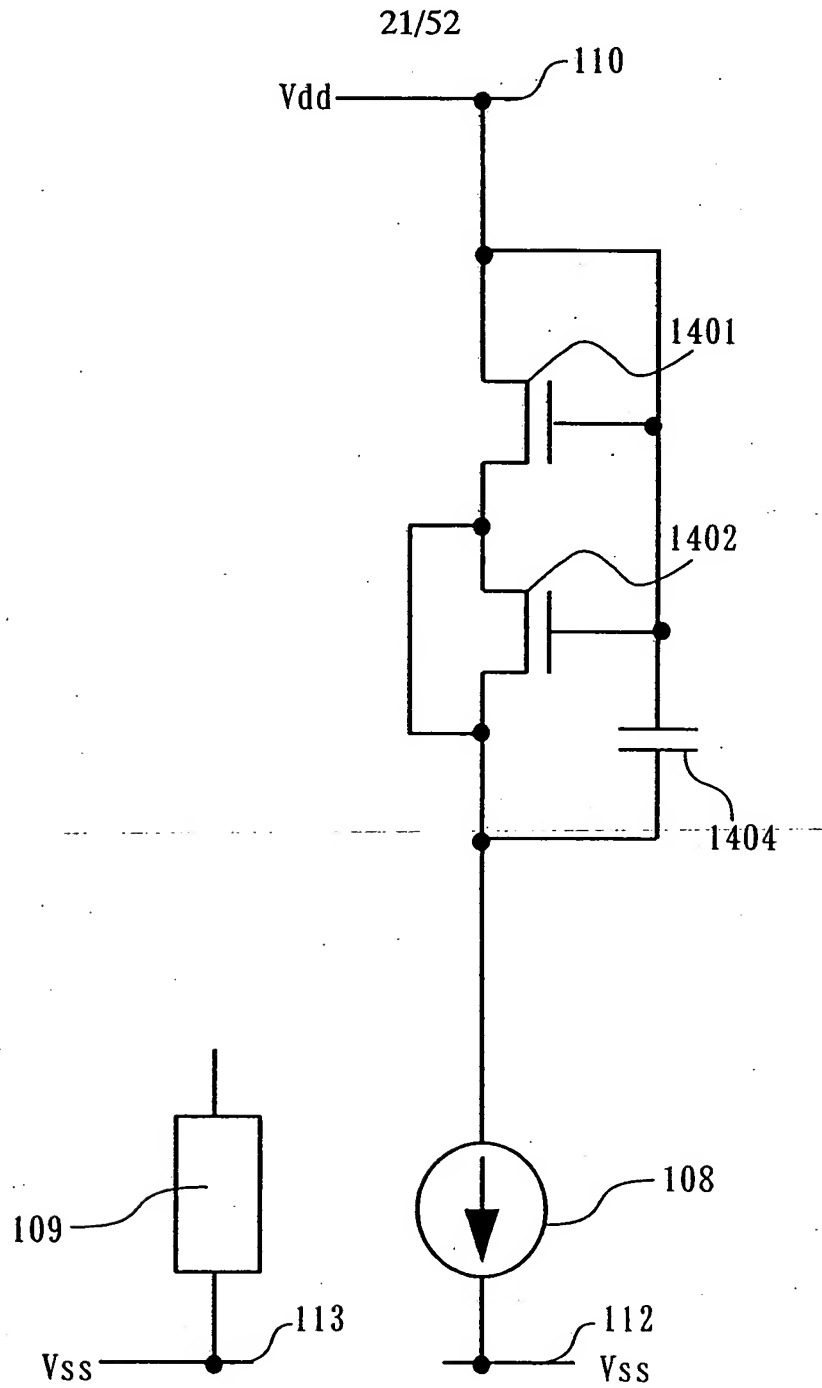


FIG. 21

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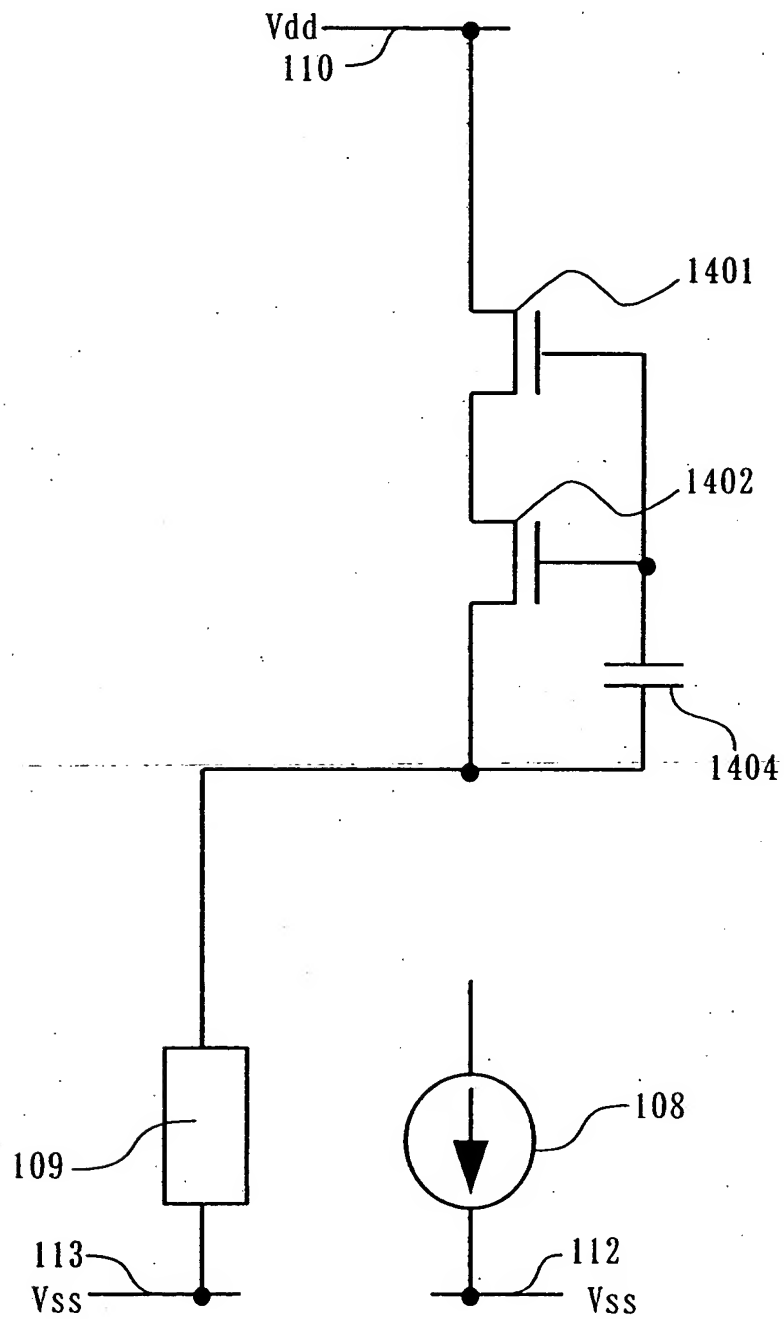


FIG. 22

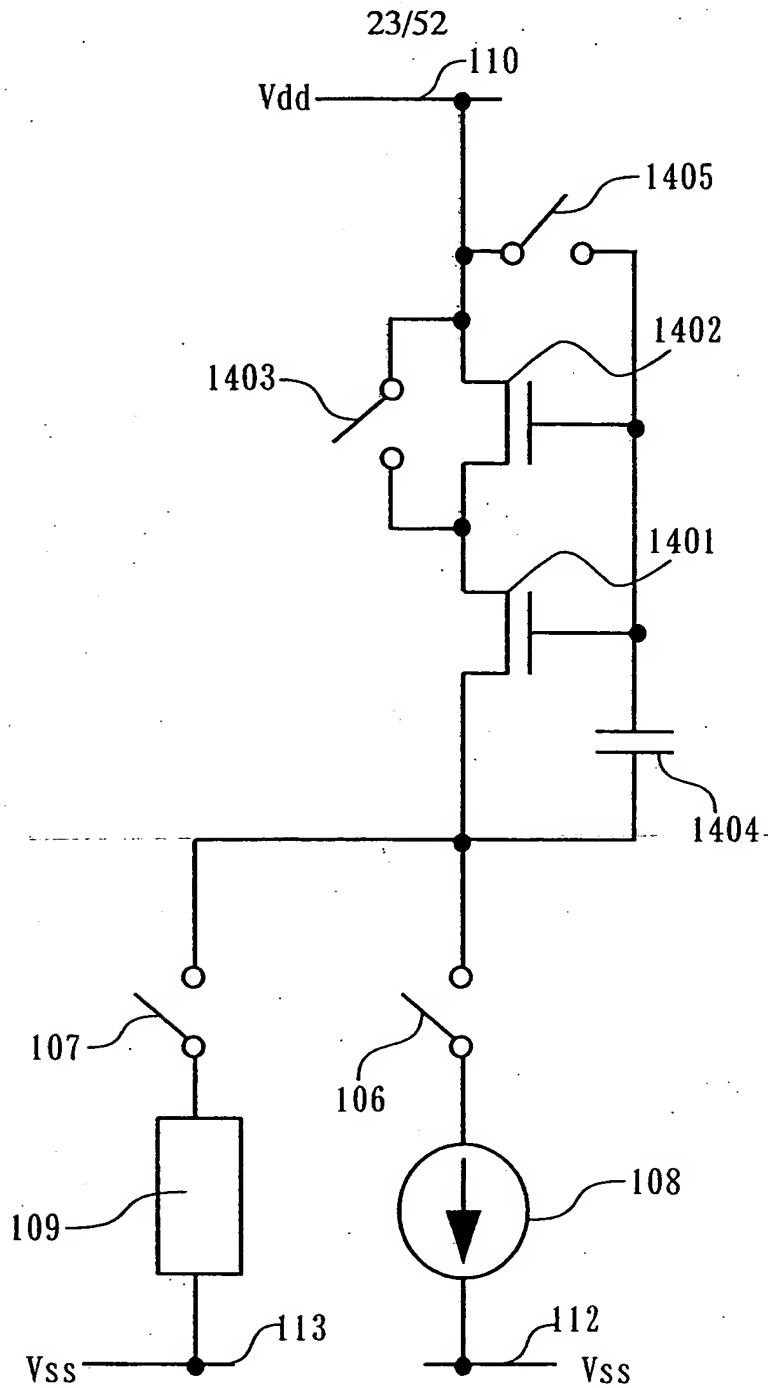


FIG. 23

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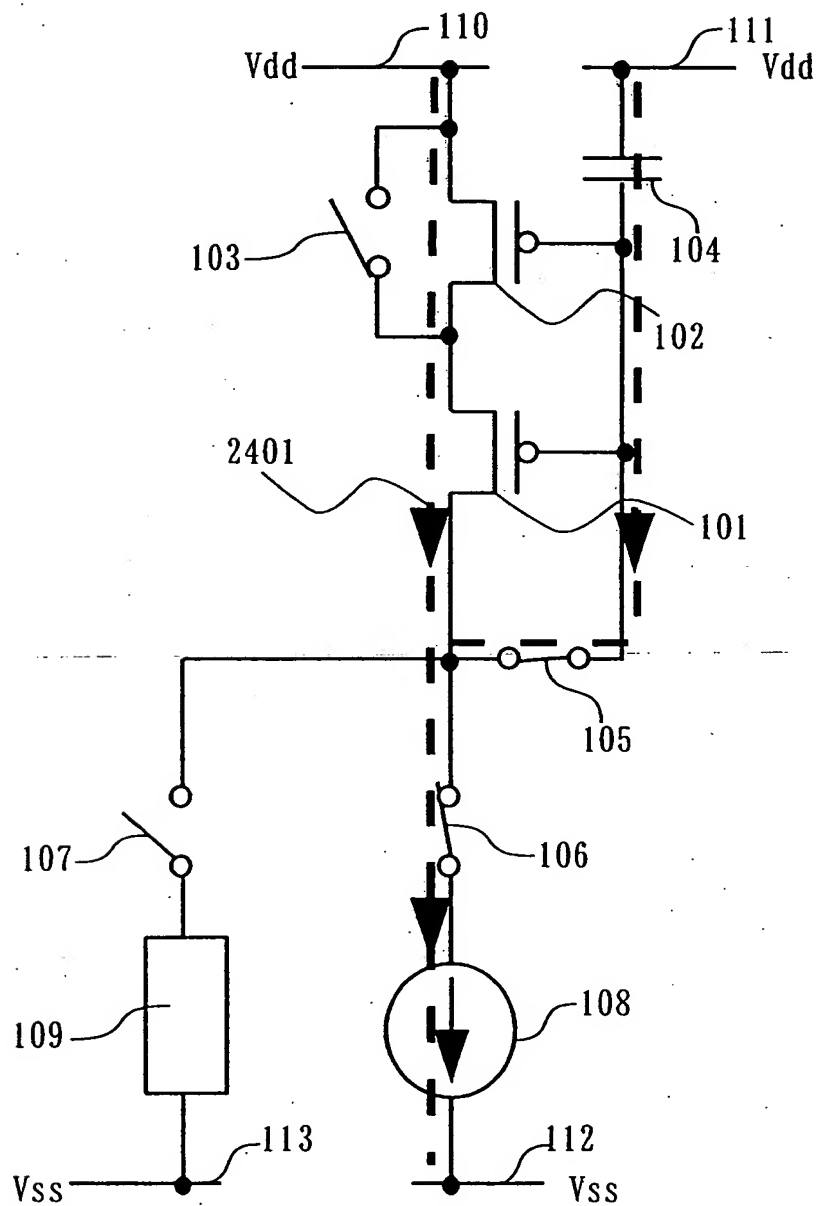


FIG. 24

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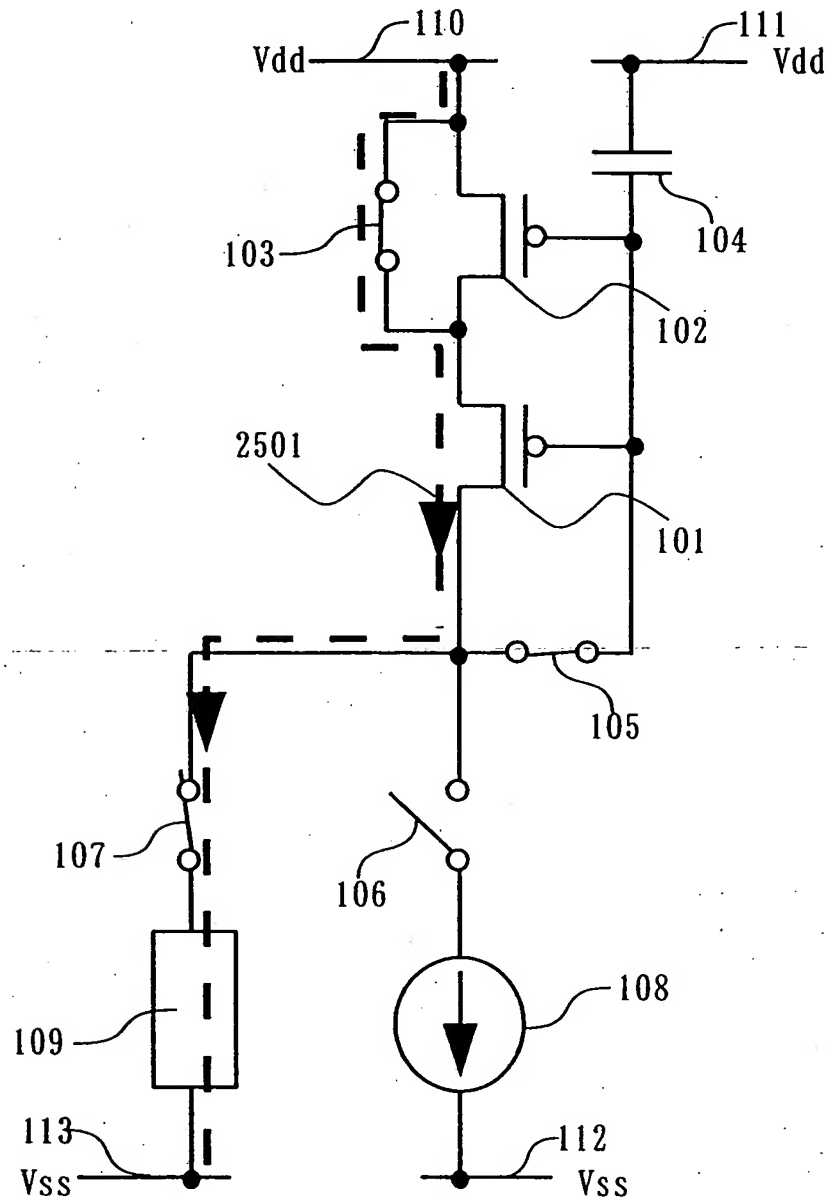


FIG. 25



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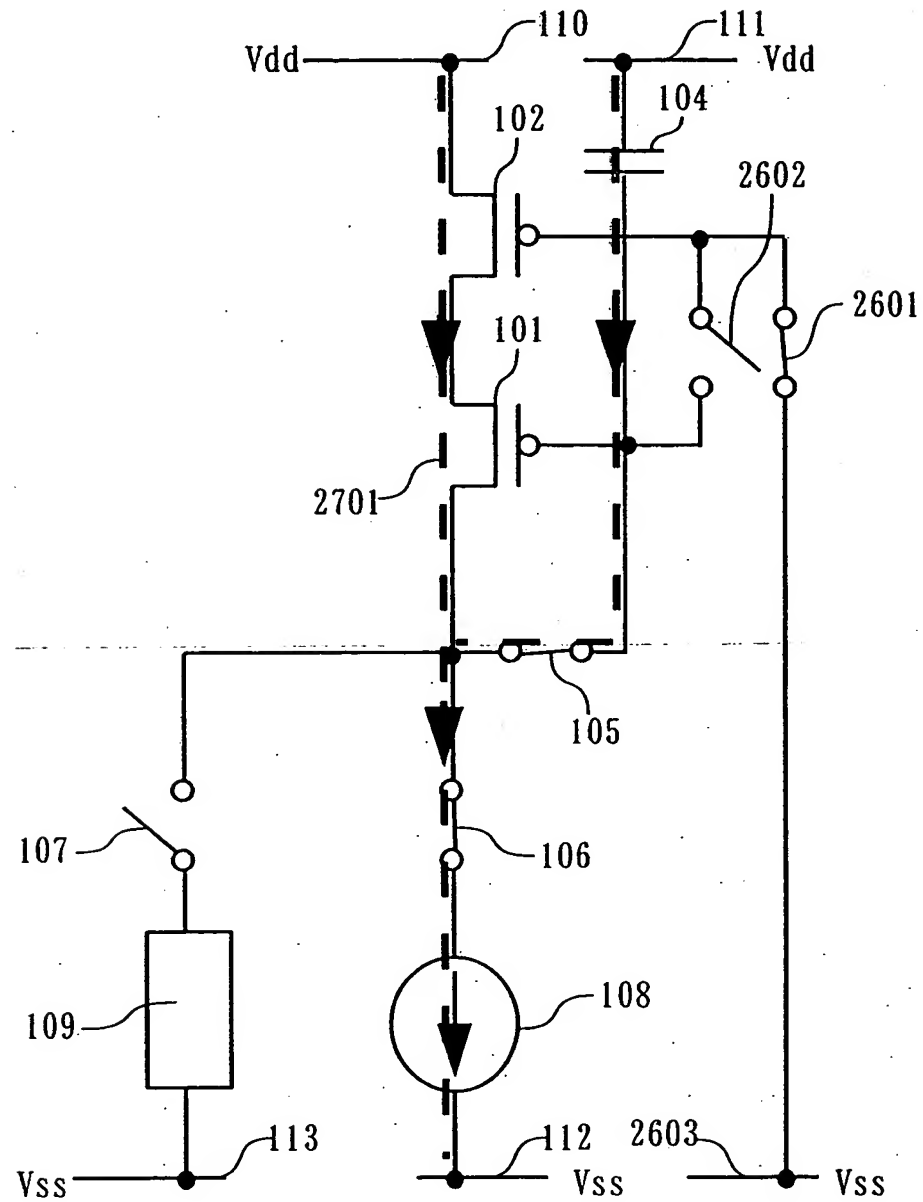


FIG. 27

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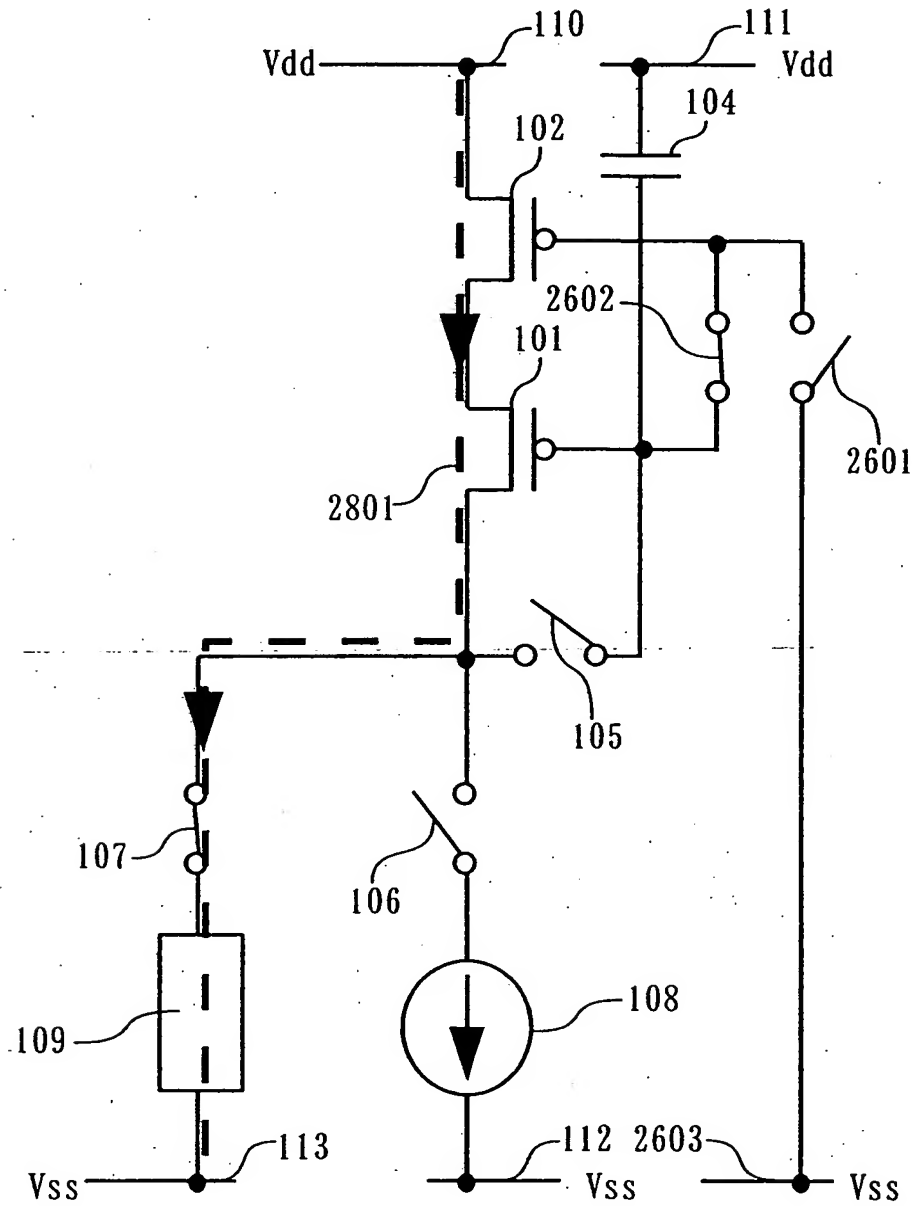


FIG. 28

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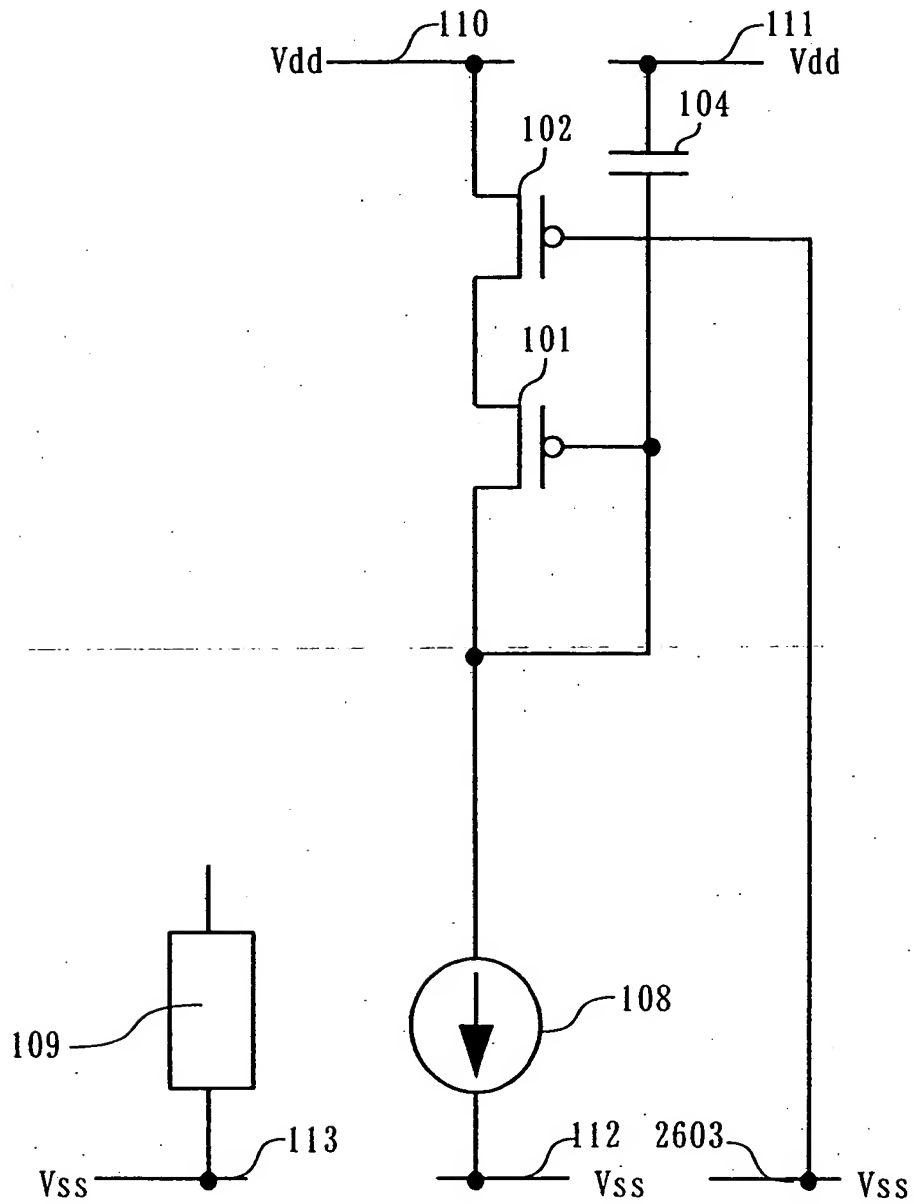


FIG. 29

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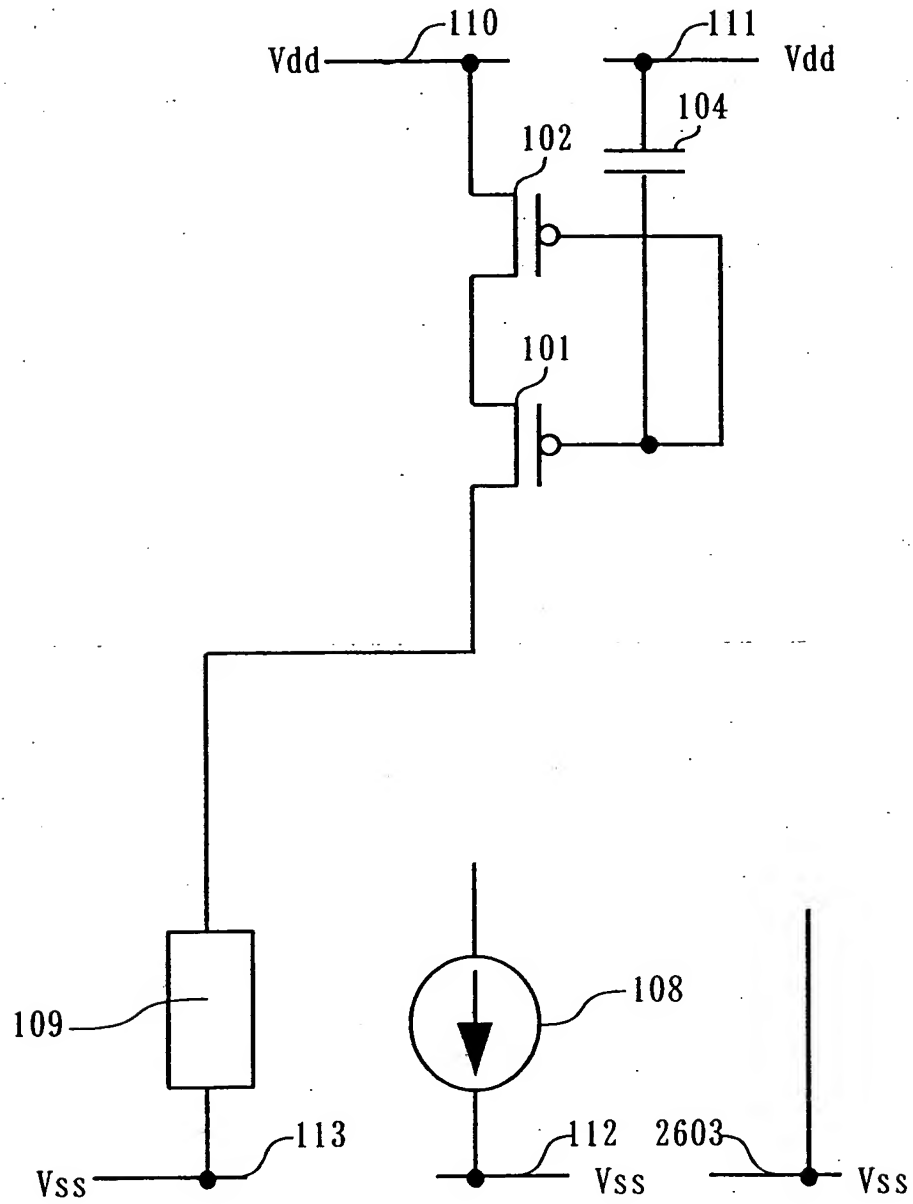


FIG. 30

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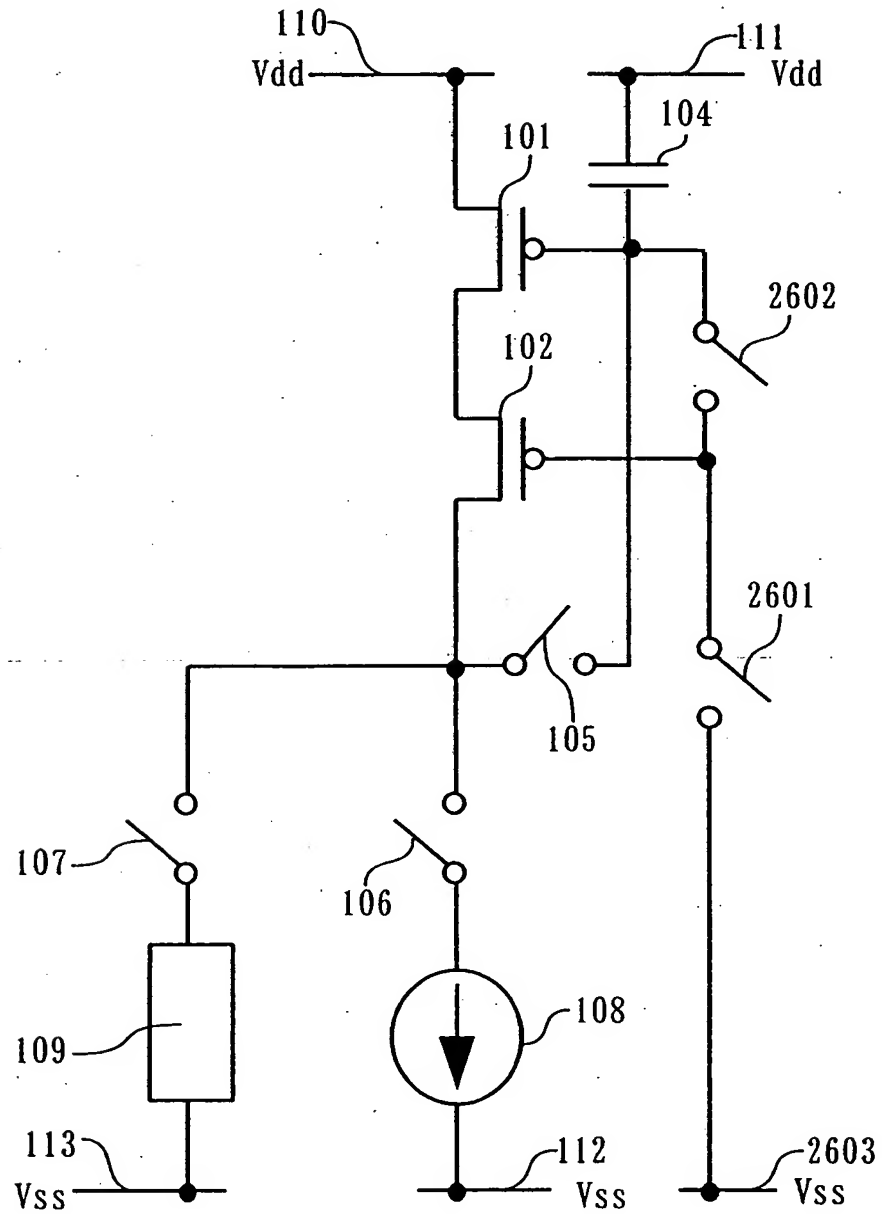


FIG. 31

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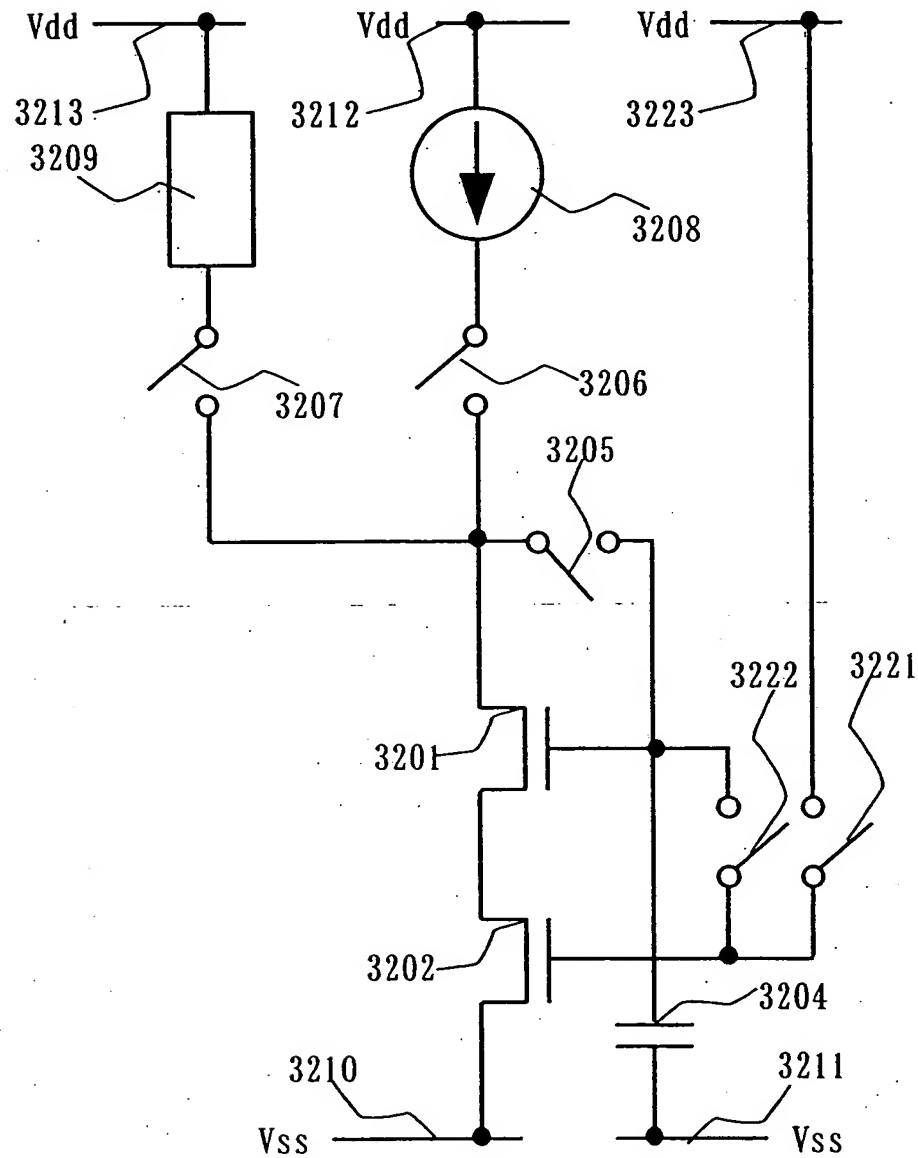


FIG. 32

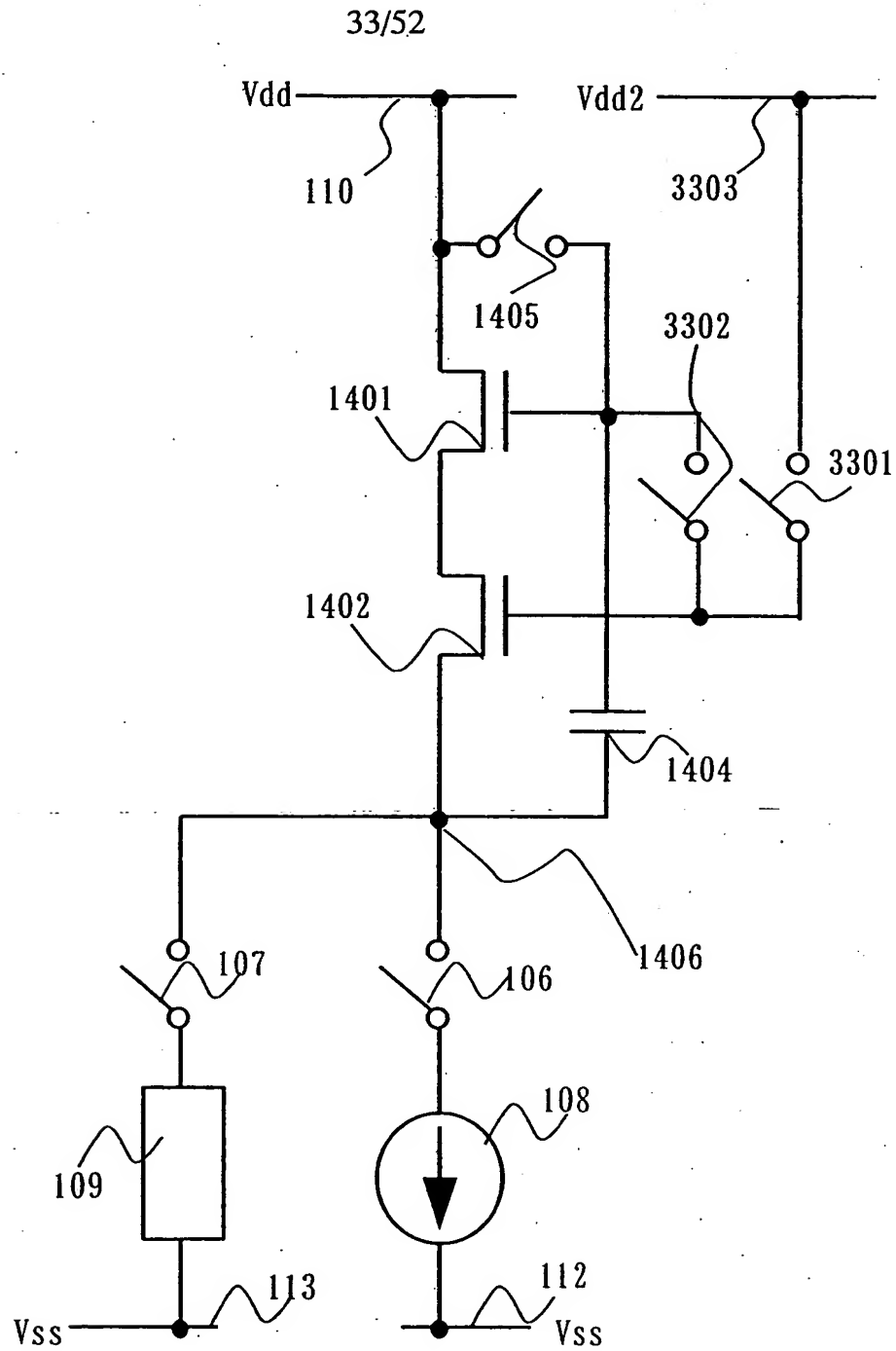


FIG. 33



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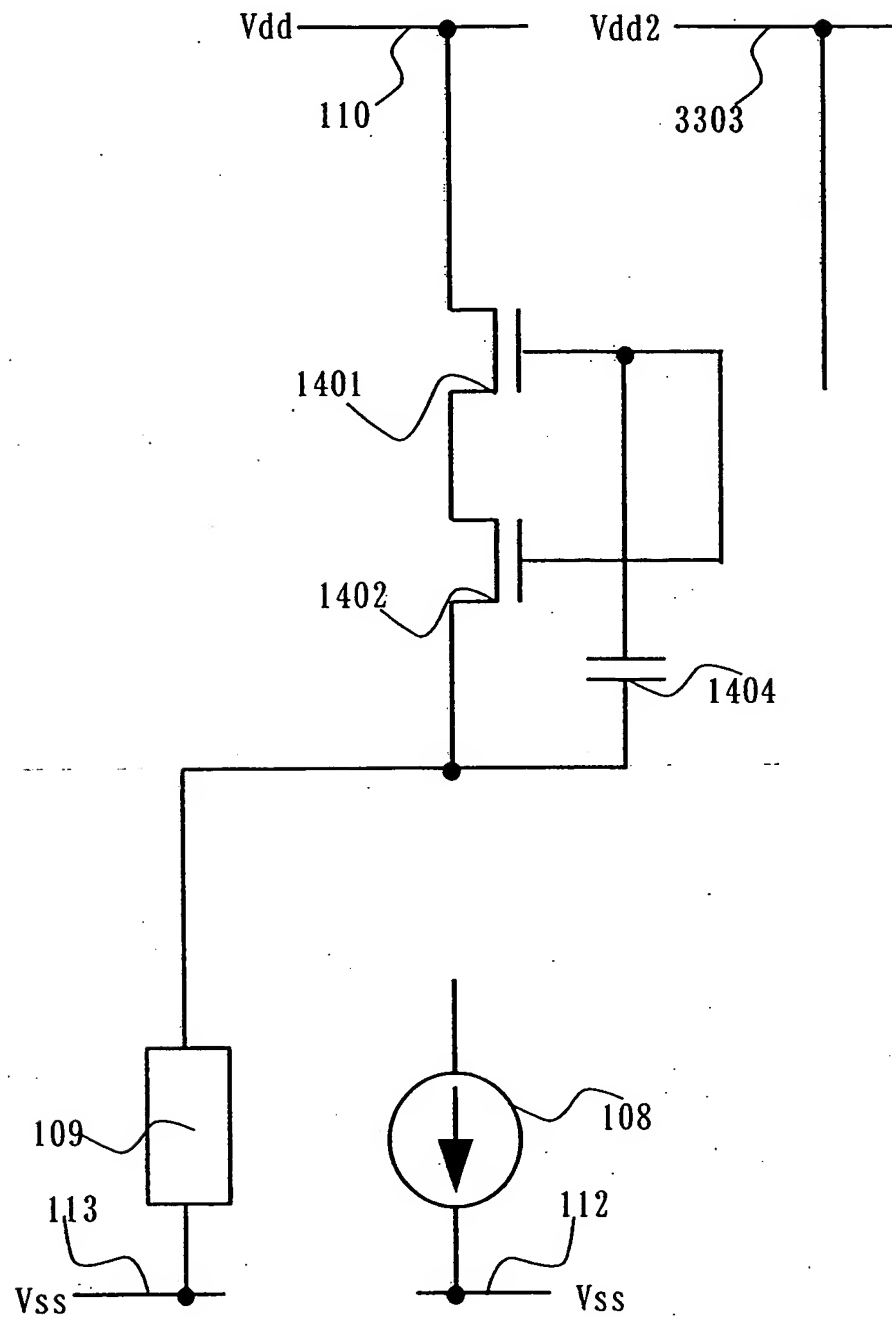


FIG. 35

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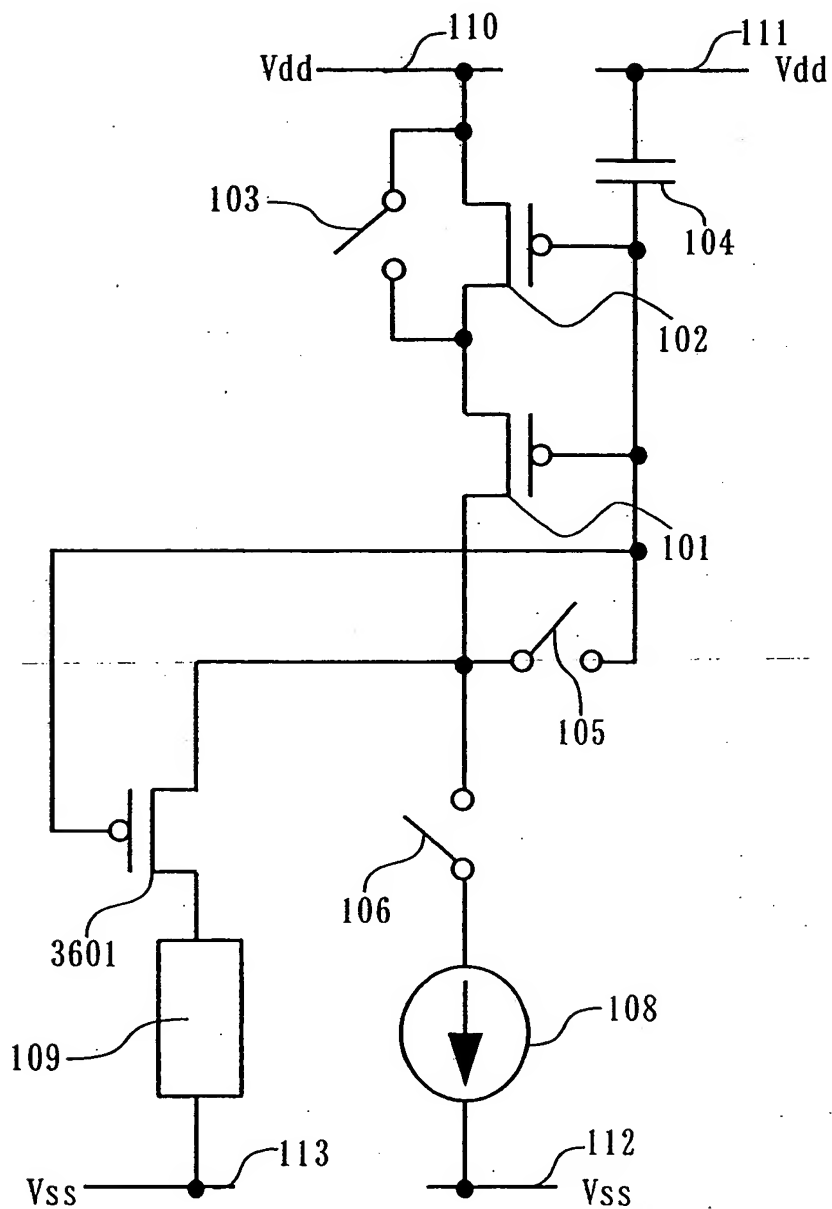


FIG. 36

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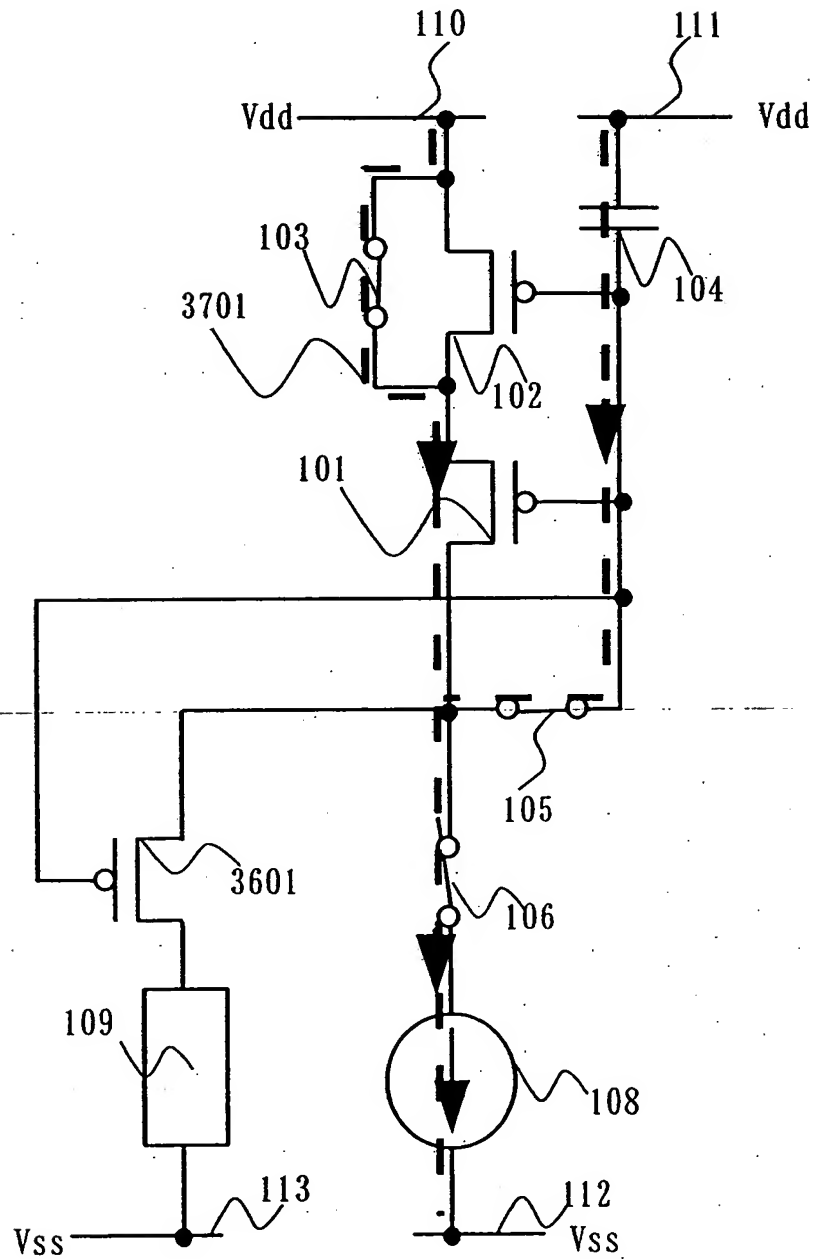


FIG. 37

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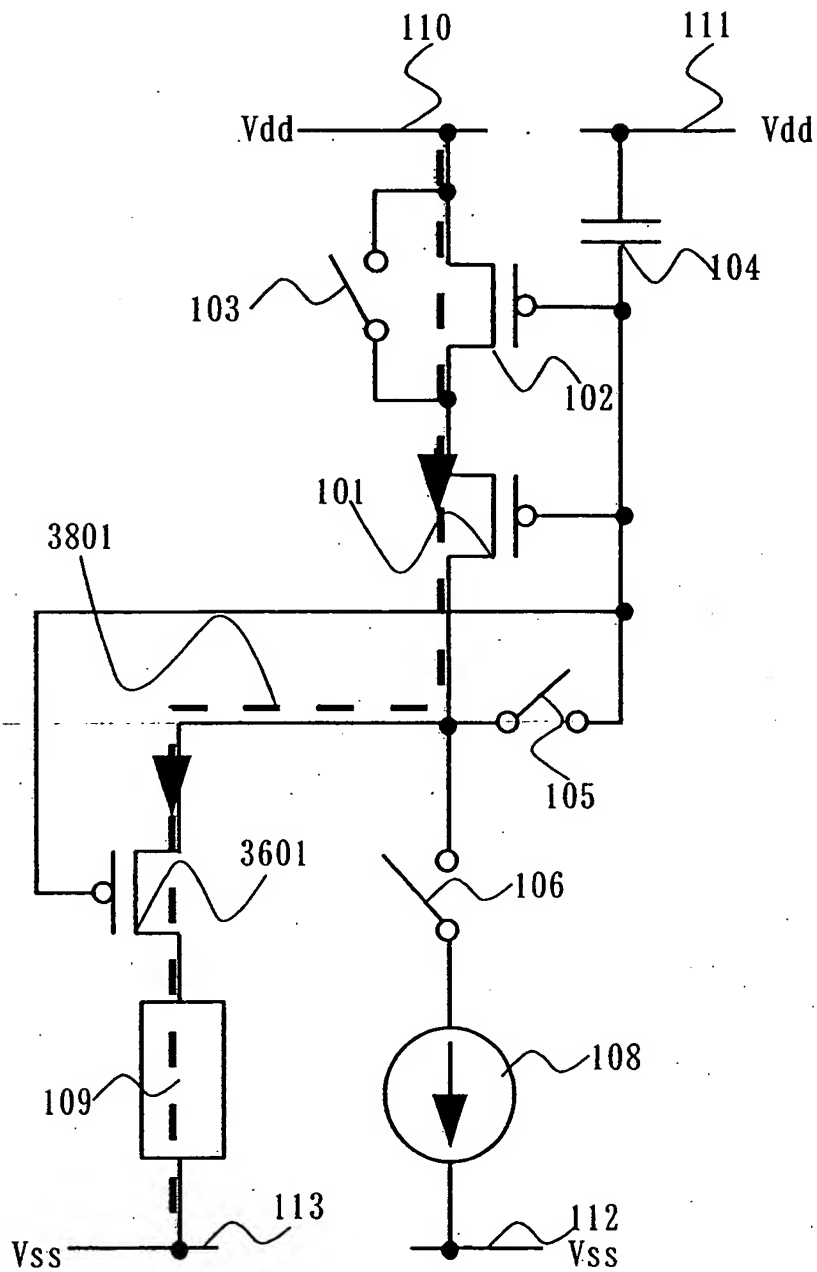


FIG. 38

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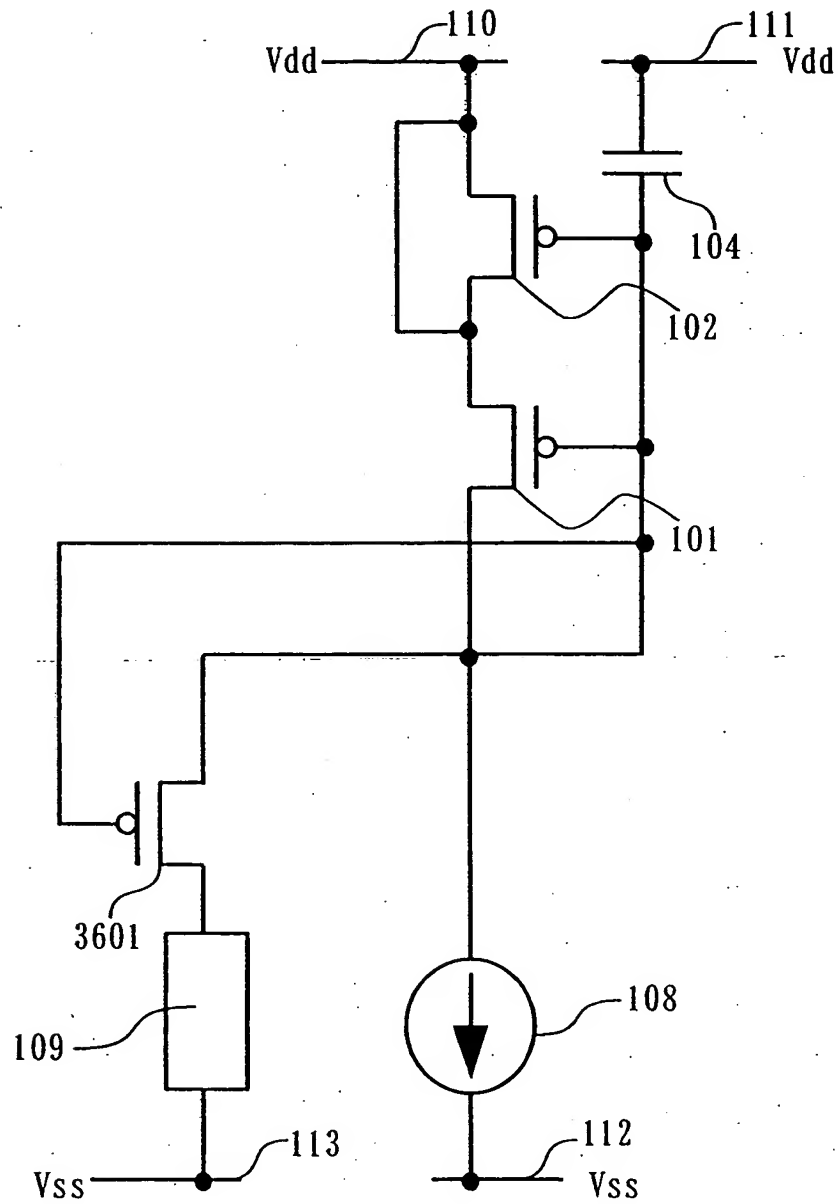


FIG. 39

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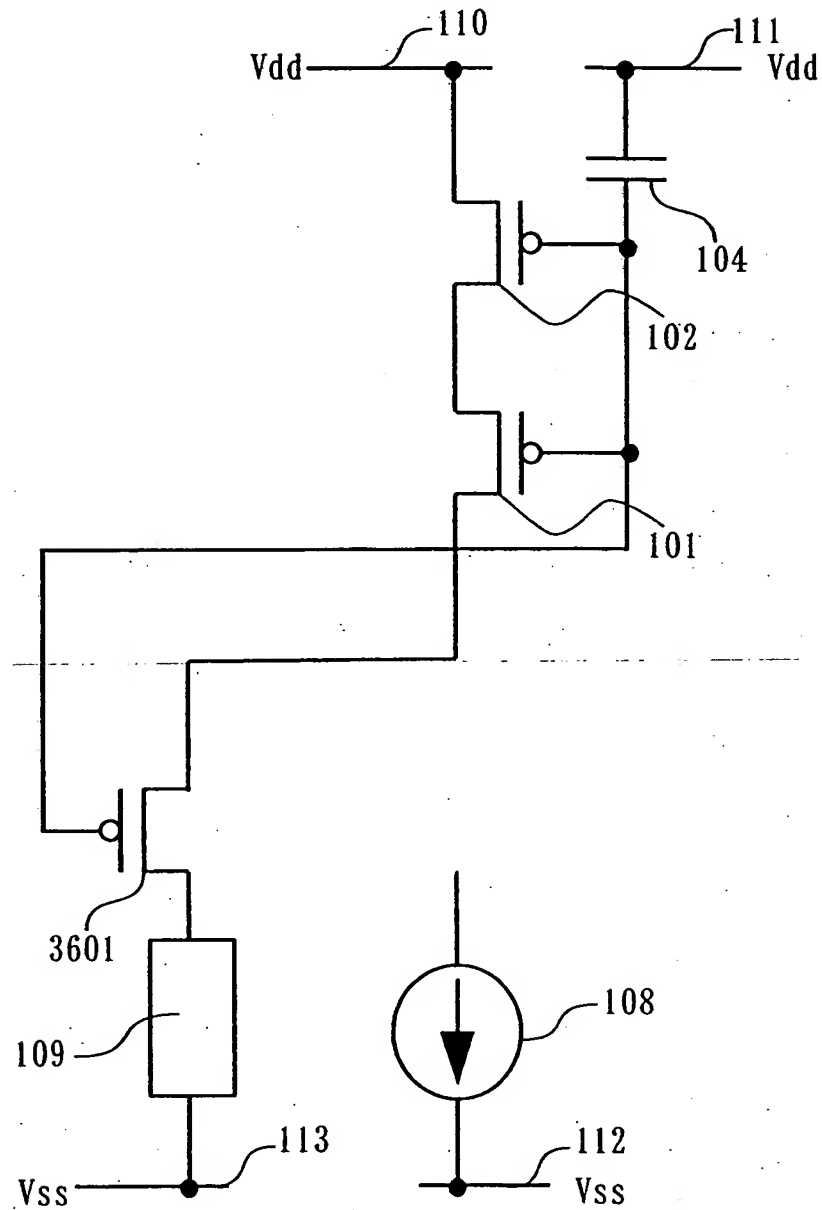


FIG. 40

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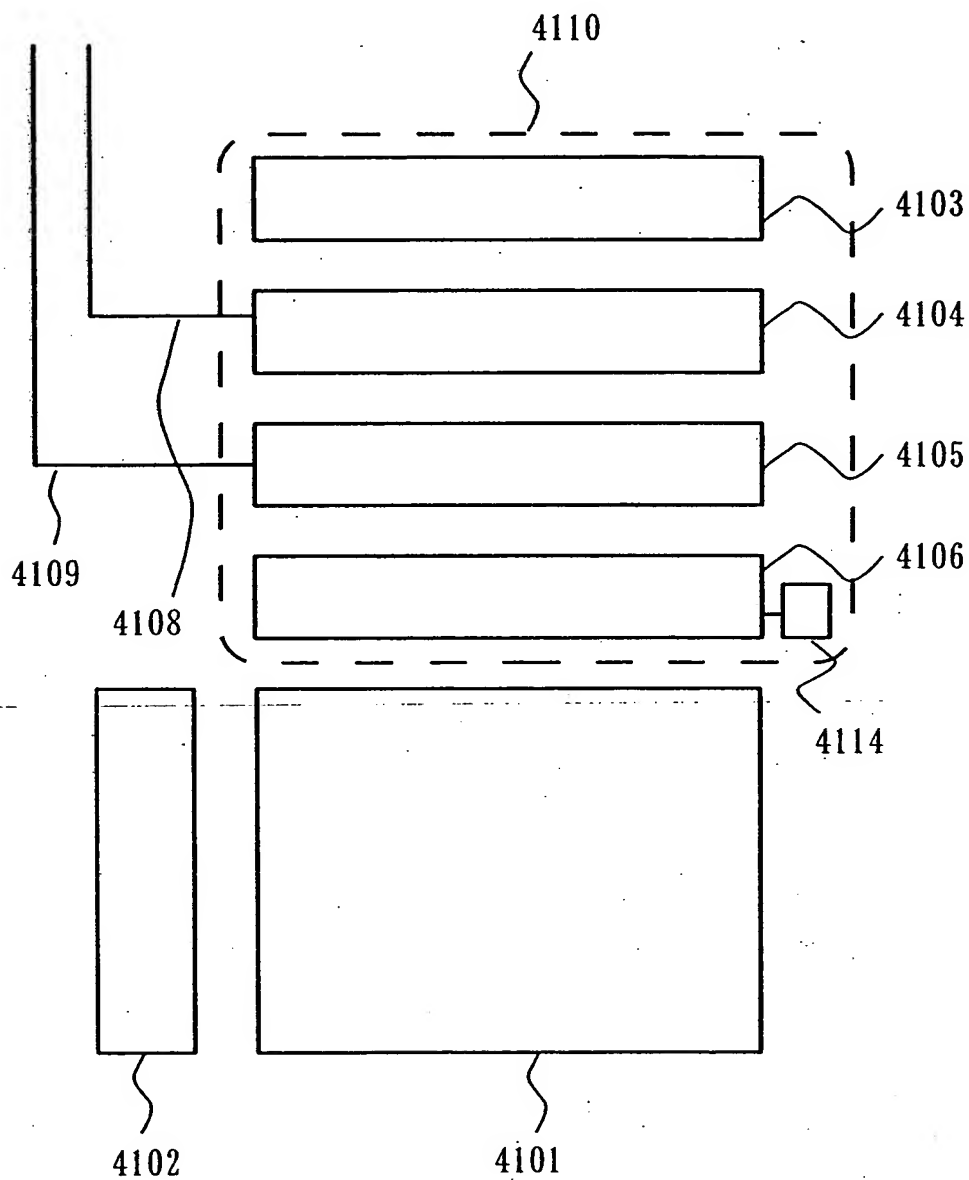


FIG. 41

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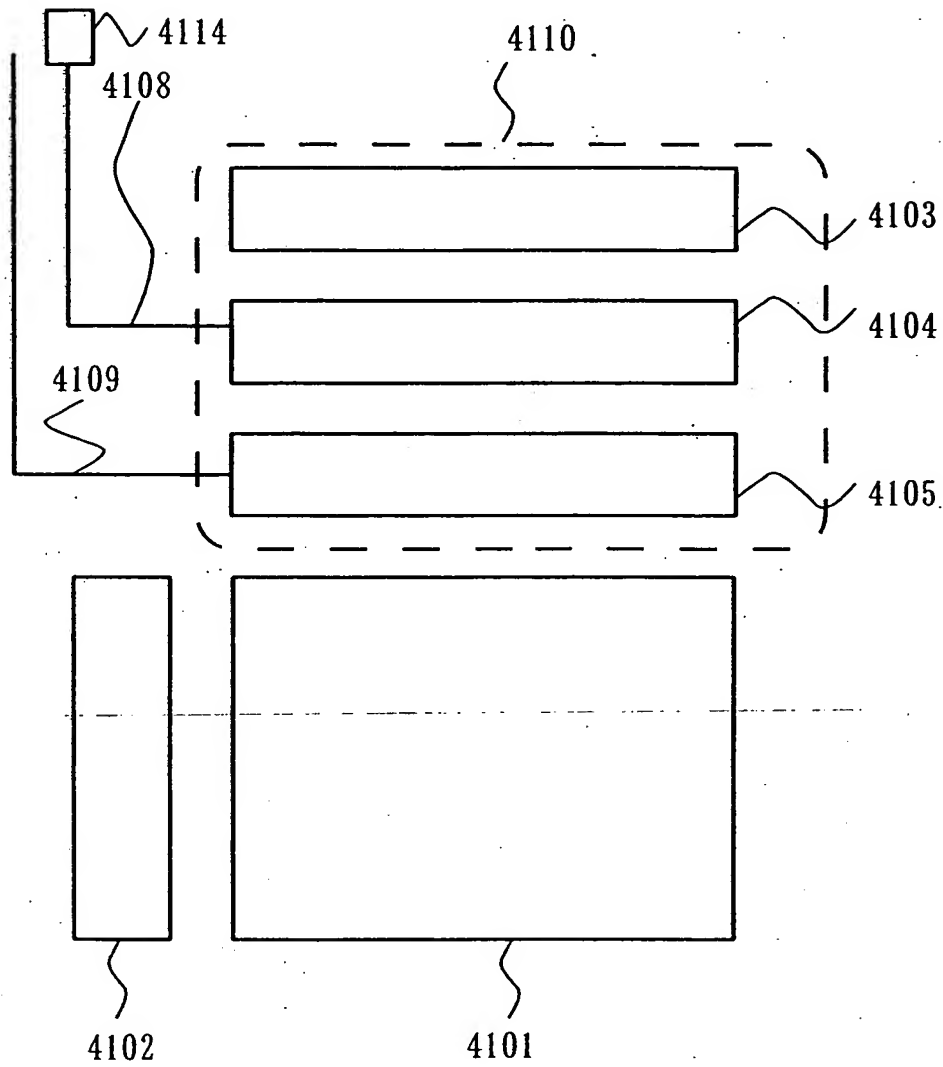


FIG. 42

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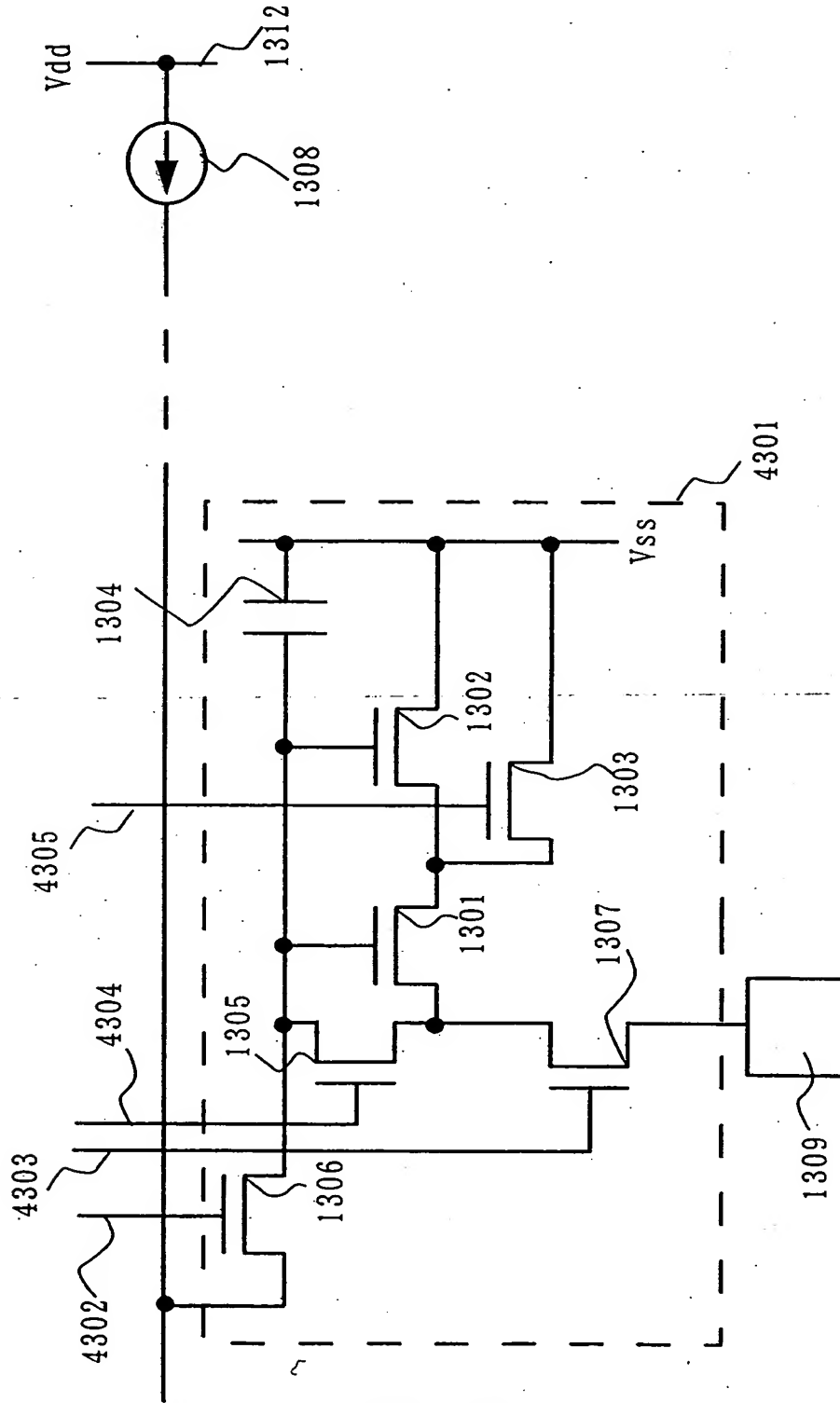


FIG. 43

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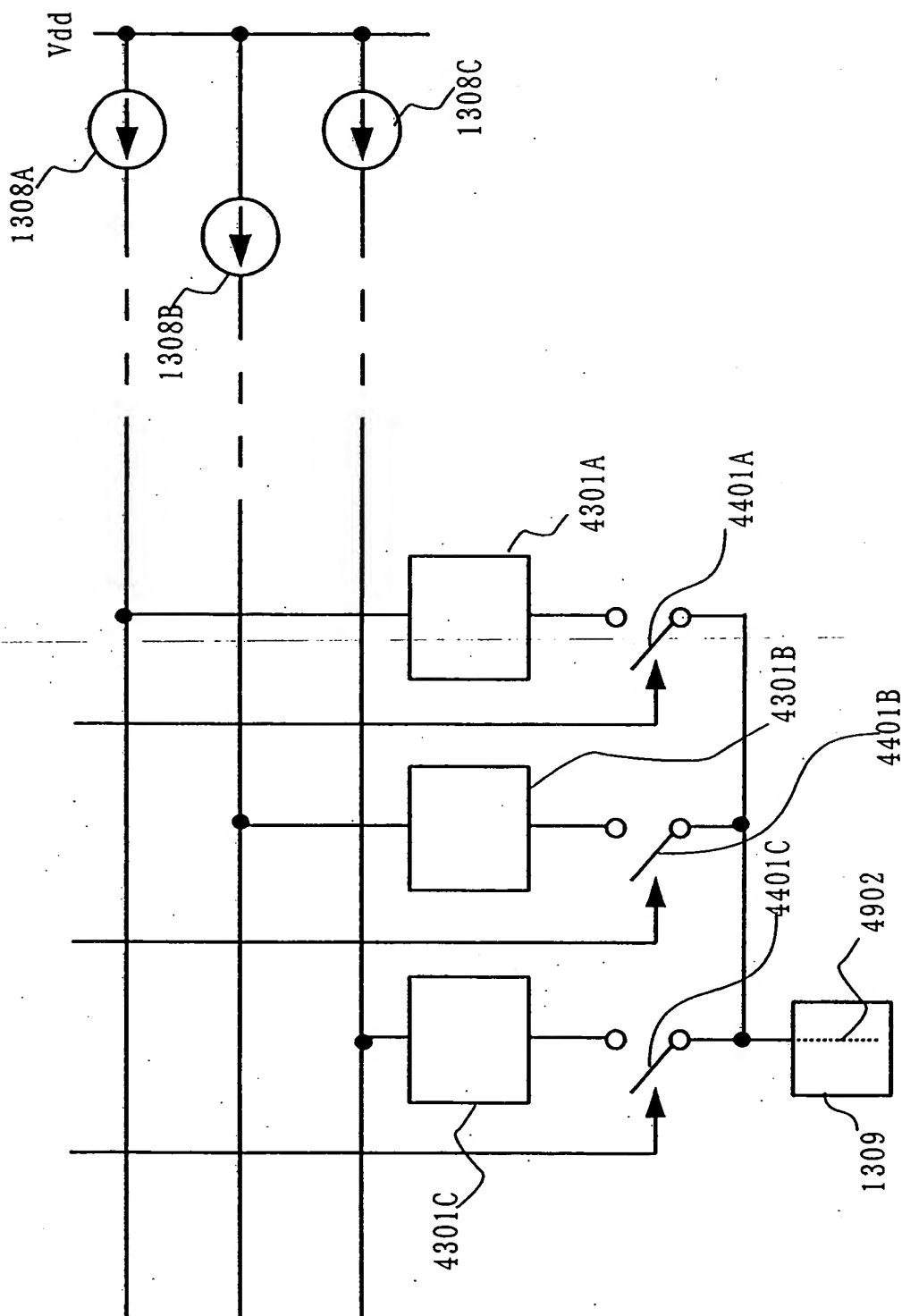


FIG. 44

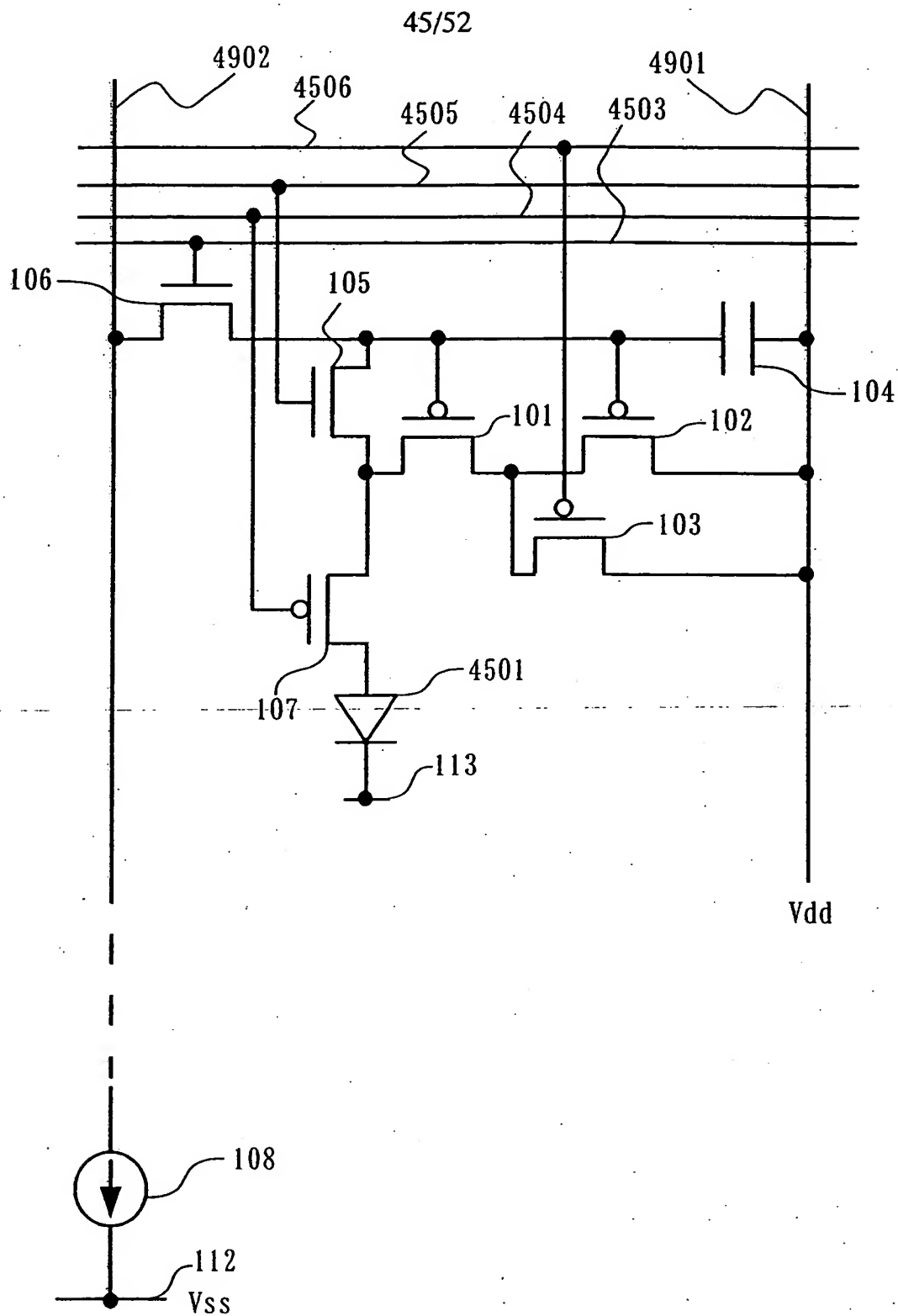


FIG. 45

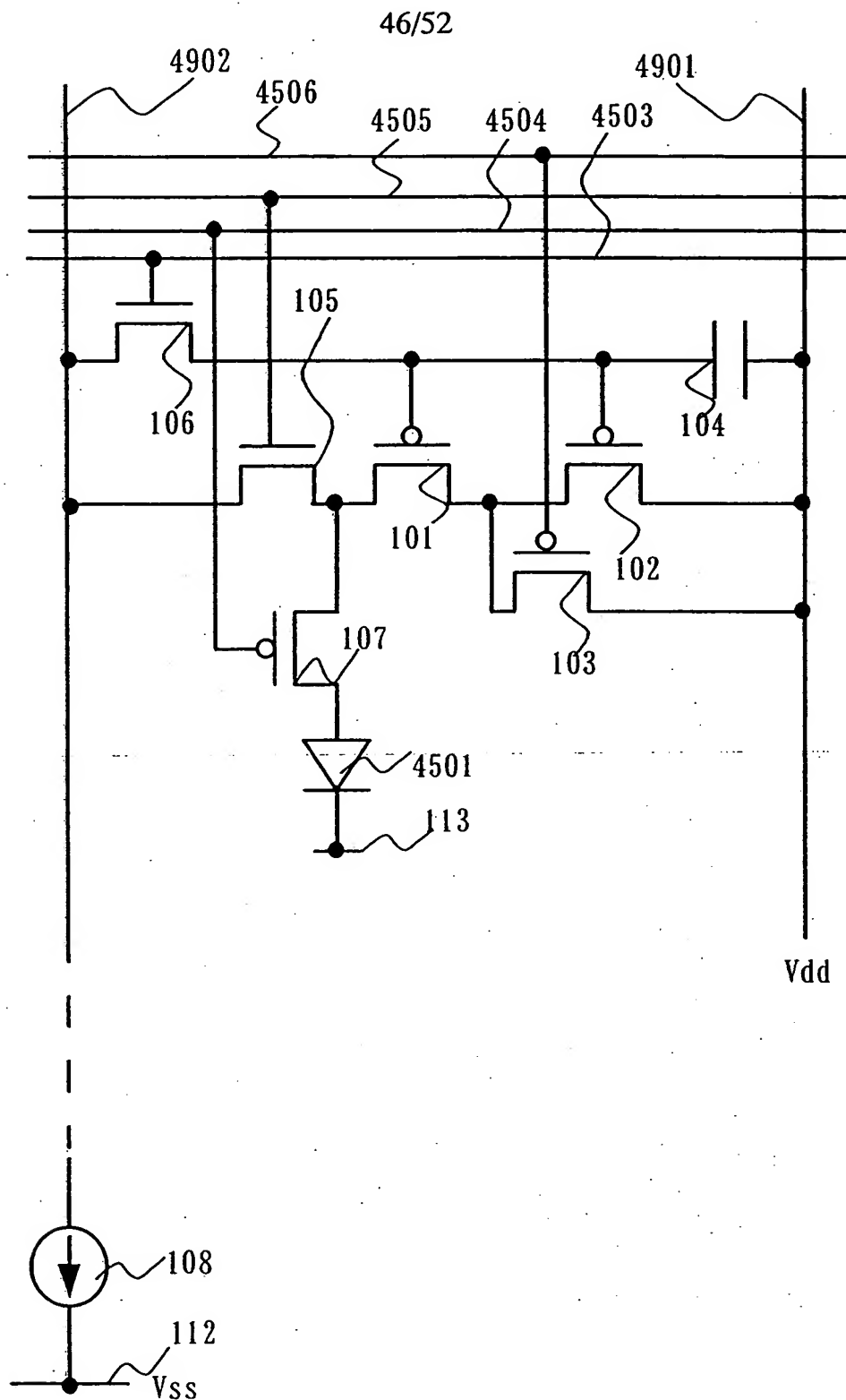


FIG. 46

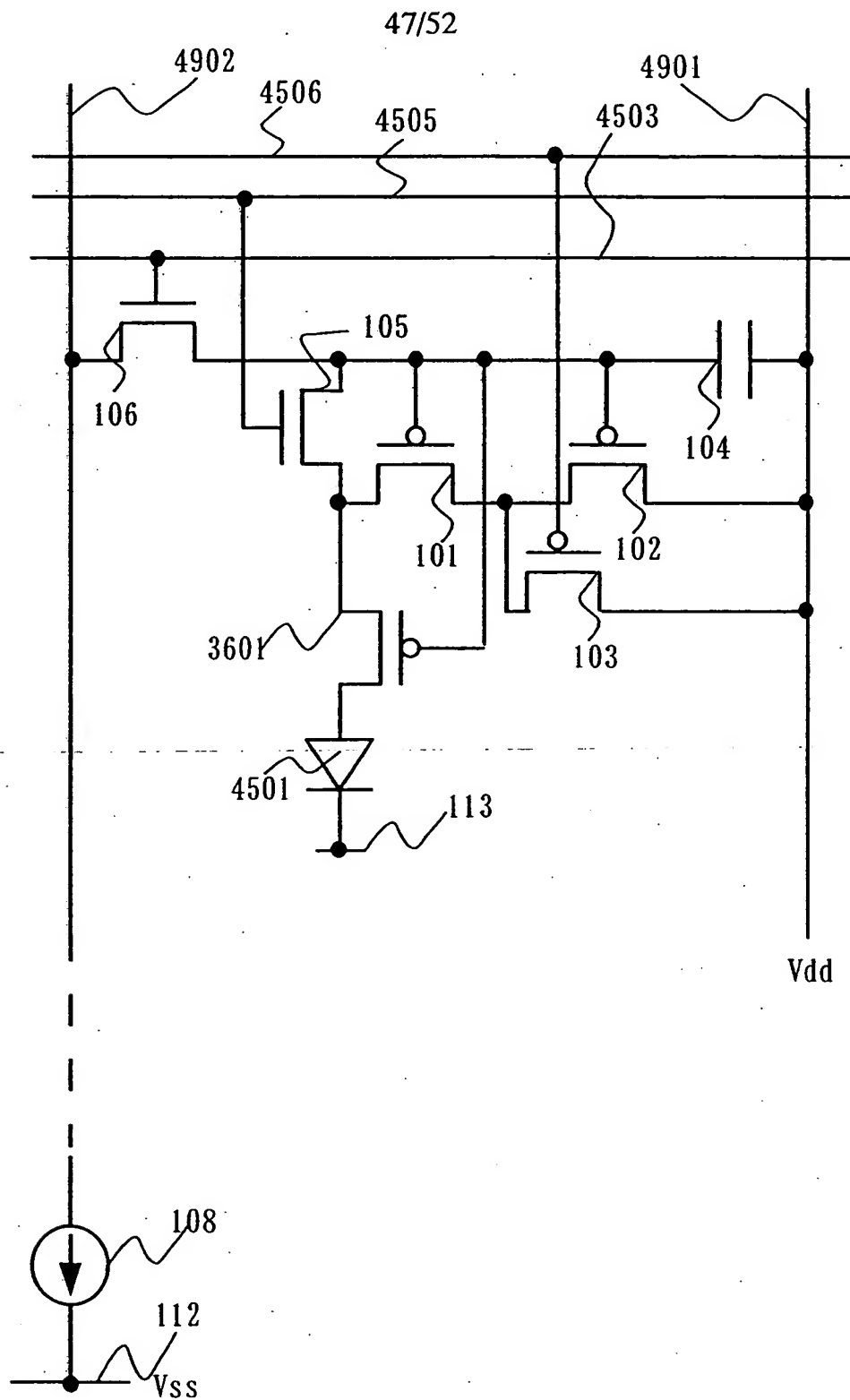


FIG. 47

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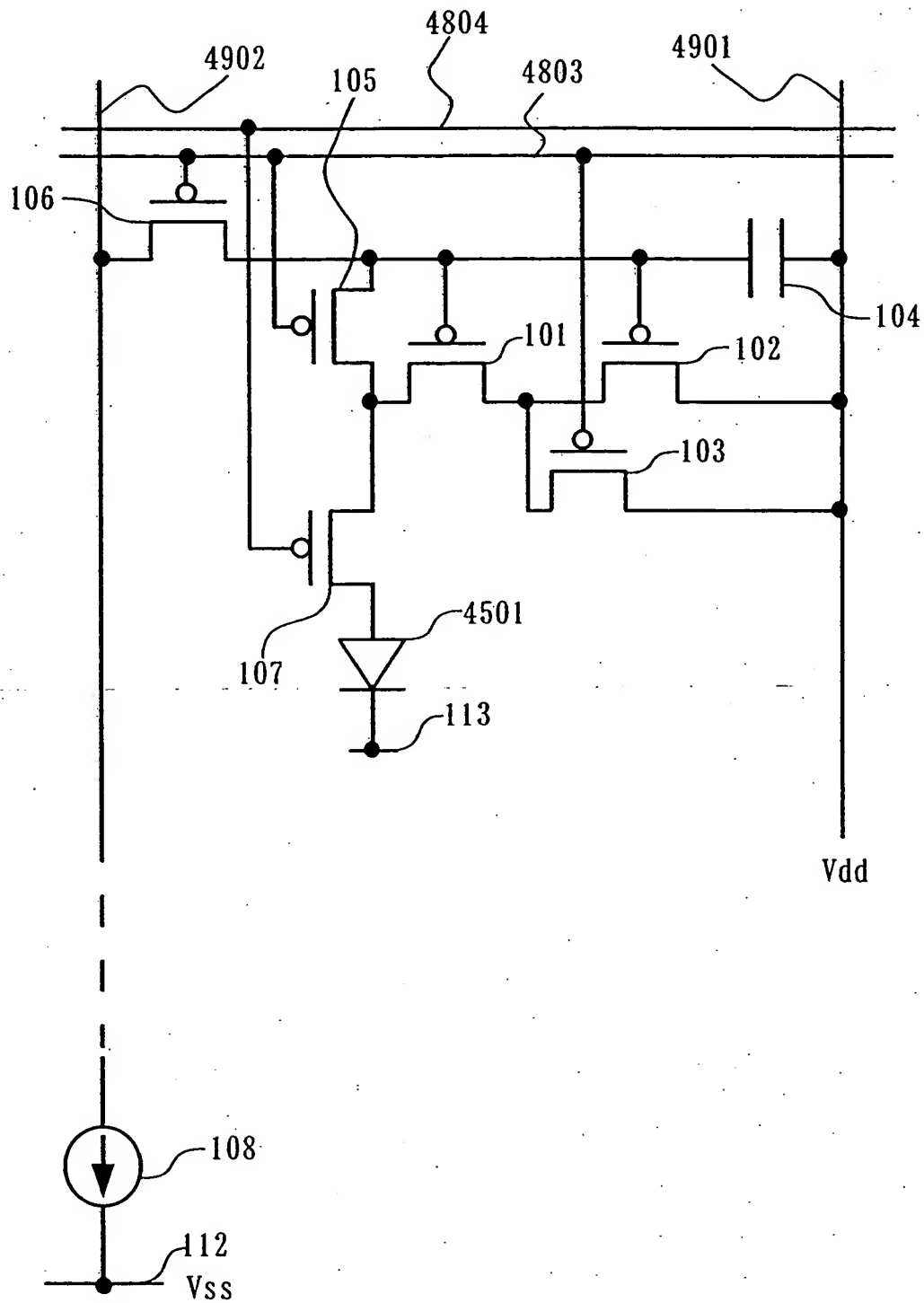


FIG. 48



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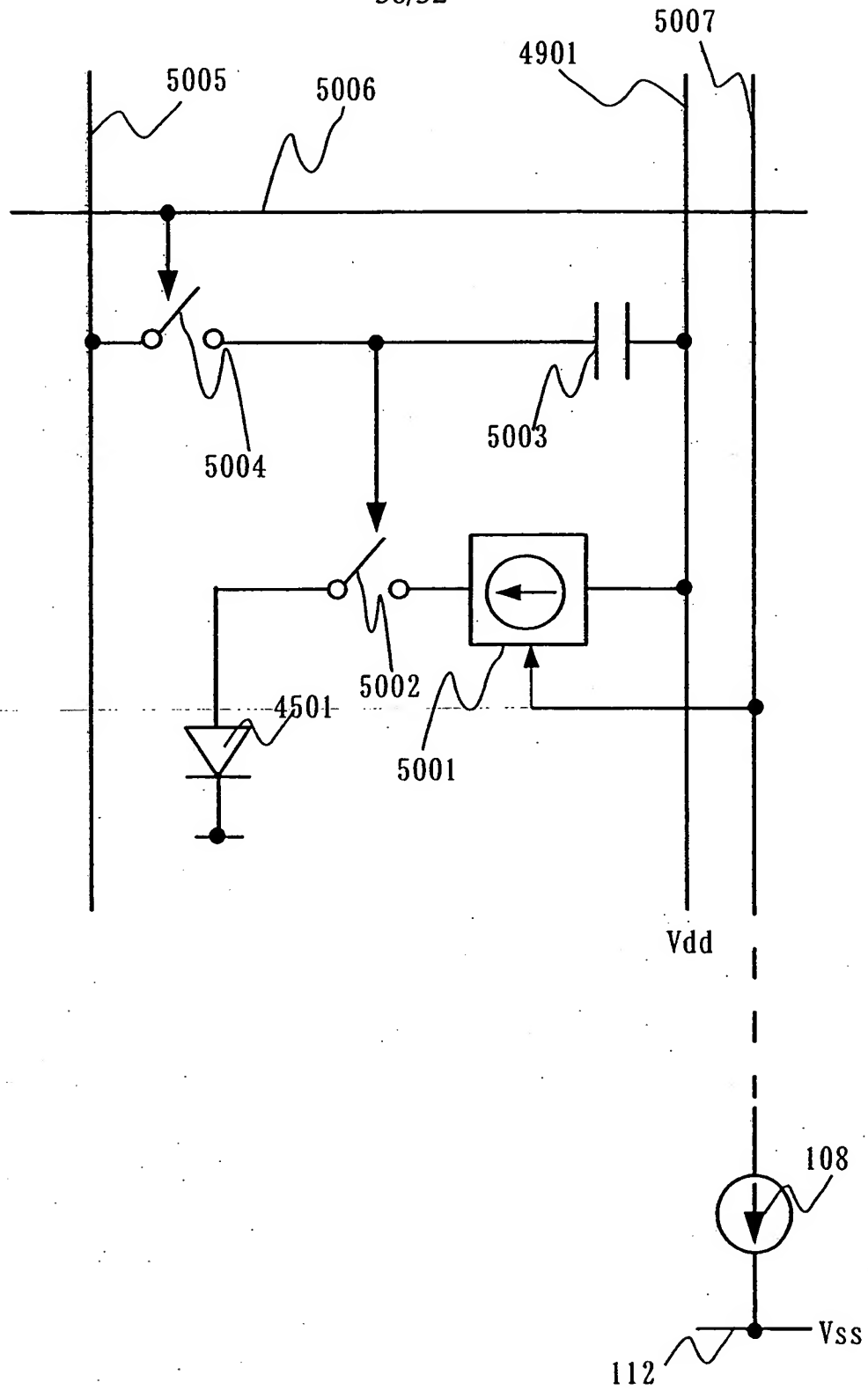


FIG. 50



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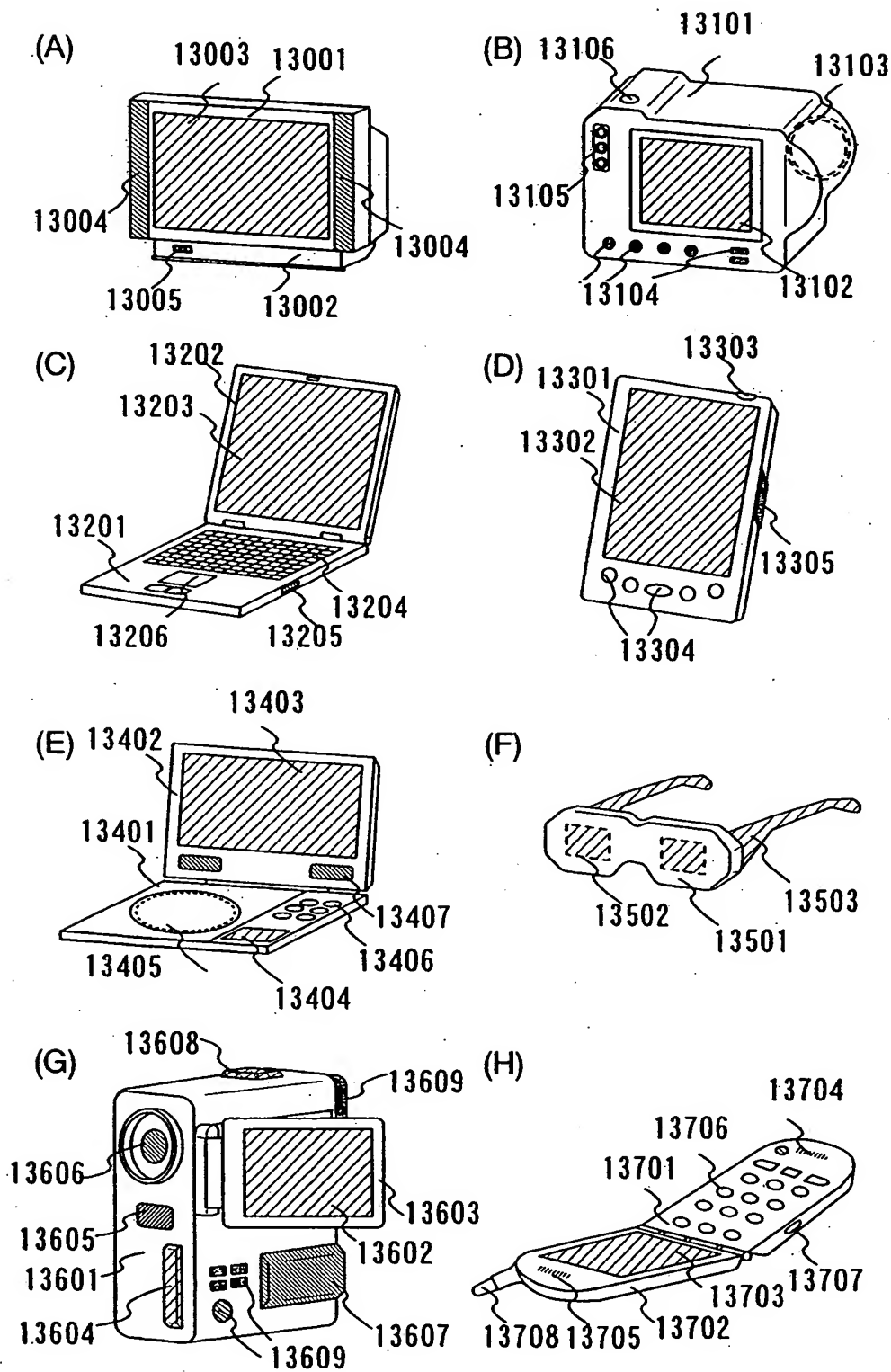


FIG. 52

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